# On-chip receiver RF front-end 

Torleif Skår



Thesis submitted for the degree of
Master in Electrical Engineering, Informatics and Technology
(Microelectronics and sensor technology) 60 credits

## Department of Physics

Faculty of mathematics and natural sciences

## UNIVERSITY OF OSLO

# On-chip receiver RF front-end 

Torleif Skår

© 2020 Torleif Skår

On-chip receiver RF front-end
http://www.duo.uio.no/

Printed: Reprosentralen, University of Oslo

First and foremost, I would like to express my sincere gratitude to my supervisors, Sumit Bagga \& Dag T. Wisland. Thanks for both the opportunity to take on this thesis, as well as providing me with your scientific insight in the field of RFIC design. This wouldn't have been possible without you.
I would also like to extend my thanks to Kristian Kjelgård at the Nanoelectronics group for helping out with all lab related things, whether it would be PCBs, wire-bonding, wafer-probing or VNAs, you've been of tremendous help.

A special thanks to the people at Integrand Software for providing me with a license for their electromagnetic simulator, EMX. This was crucial for making the custom passive structures in this thesis.
Last, but not least: a special thanks to friends and family for keeping me sane during this work.

Thanks,
Torleif Skår

## Abstract

IEEE 802.15.4z (UWB) is viewed as an alternative technology for the Internet-of-Things (IoT). Having matured over the last two decades, it has found its niche as a low-cost, low-power technology. Today, UWB technology is synonymous with asset tracking (RTLS) and vital sensing (radar).
As UWB devices share precious spectrum resources with existing wireless RF systems (e.g., at 5 GHz ), filters ensure reliability. Low noise amplifiers (LNAs) play an important role in the RF front-end (RFFE) by providing sufficient gain to keep the system noise figure low, as well as impedance matching for maximum power transfer. Co-designed with the filter, a frequency-selective LNA is required to reject out-of-band frequencies to further enhance the sensitivity of the RFFE. In the design of these radio frequency integrated circuits (RFICs), monolithic inductors/transformers are vital components.

## Contents

1 Introduction ..... 1
2 High-Pass Filter Design ..... 9
2.1 Introduction ..... 9
2.2 Order ..... 10
2.3 Center-frequency, cut-off frequency \& passband ..... 11
2.4 Specifications ..... 11
2.5 DHT filter: pseudo-differential to pseudo-differential ..... 12
2.5.1 Simulation results ..... 14
2.6 SHT filter: single-ended to differential ..... 19
2.6.1 Simulation results ..... 24
2.7 Conclusion ..... 28
3 Low-Noise Amplifier Design ..... 33
3.1 Introduction ..... 33
3.2 Circuit design ..... 33
3.3 Simulation results ..... 47
3.4 Conclusion ..... 51
4 Area Optimized and Passively Reconfigurable RF Front-Ends ..... 55
4.1 Area-optimized RFFE ..... 56
4.1.1 Simulation results ..... 58
4.2 Passively reconfigurable RFFE ..... 65
4.3 Simulation results ..... 70
4.3.1 Half-duplex mode ..... 70
4.3.2 Full-duplex mode ..... 77
4.3.3 Summary ..... 85
5 Measurements ..... 87
5.1 Passively reconfigurable RFFE ..... 88
5.1.1 Half-duplex mode ..... 88
5.1.2 Full-duplex mode ..... 94
5.2 Area-optimized RFFE ..... 101
5.3 Summary ..... 107
6 Conclusion ..... 109

## List of Figures

1.1 Apple's U1 UWB chip for communications [1] ..... 1
1.2 Decawave's DW1000 IC for RTLS [3] ..... 2
1.3 Novelda's human presence sensor based on the X 4 CMOS IC [4] ..... 2
1.4 Block diagrams of the Novelda X4 IC [5 ..... 3
1.5 HPF with Q-enhancement [5] ..... 4
1.6 Wideband LNA [5] ..... 4
1.7 Measured RF front-end gain of the HPF, LNA and pre- amplifier stages (buffers) [5] ..... 5
1.8 Full schematic of the BDFC-based front-end [6] ..... 6
1.9 Block diagram of full-duplex single-ended RFFE ..... 7
2.1 Schematic for the PDPD-Filter ..... 13
2.2 Half-circuit layout for the PDPD-Filter ..... 14
$2.3 \quad S_{11}$ for the PDPD elliptic filter. ..... 15
2.4 Real input impedance of the PDPD elliptic filter. ..... 16
2.5 Imaginary input impedance of the PDPD elliptic filter. ..... 16
$2.6 S_{22}$ for the PDPD elliptic filter. ..... 17
2.7 Real output impedance of PDPD elliptic filter. ..... 17
2.8 Imaginary output impedance of PDPD elliptic filter. ..... 18
2.9 Insertion loss \& NF from the PDPD elliptical filter. ..... 18
2.10 Transfer function of the PDPD elliptical filter. ..... 19
2.11 Group delay of the PDPD elliptical filter. ..... 20
2.12 Schematic for the SEDE elliptic filter ..... 21
2.13 Half-circuit layout for the SEDE-Filter ..... 23
2.14 Transformer $T_{1}$ from Fig. 2.12 ..... 24
$2.15 S_{11}$ for the SEDE elliptic filter. ..... 25
2.16 Real input impedance of the SEDE elliptic filter. ..... 26
2.17 Imaginary input impedance of the SEDE elliptic filter. ..... 26
$2.18 S_{22}$ for the SEDE elliptic filter. ..... 27
2.19 Real output impedance of SEDE elliptic filter. ..... 27
2.20 Imaginary output impedance of SEDE elliptic filter. ..... 28
2.21 Insertion loss \& NF from the SEDE elliptical filter. ..... 29
2.22 Transfer function of the SEDE elliptical filter ..... 29
2.23 Group delay of the SEDE elliptical filter ..... 30
3.1 The LNA, including biasing for the input stage, and output buffers. ..... 34
3.2 Biasing parameters used for the ' $g_{m} / I_{d}$ design meth- odology'. Simulated on an nMOS device with $W / L=$ $1 \mu \mathrm{~m} / 60 \mathrm{~nm}$. ..... 44
3.3 Layout of the LNA ..... 46
$3.4 \quad S_{11}$ of the LNA ..... 48
3.5 Real \& imaginary input impedances of the LNA ..... 48
$3.6 \quad S_{22}$ of the LNA ..... 49
3.7 Real \& imaginary output impedance of the LNA ..... 49
3.8 Frequency response of the LNA using S-parameter $S_{21}$ ..... 50
3.9 Reverse isolation of the LNA ..... 50
$3.10 \mu$-factor for stability ..... 51
3.11 Noise figure for the LNA ..... 52
3.12 LSSP at $f_{c}=7.875 \mathrm{GHz}$ with $P_{R F}$ from -30 dBm to 10 dBm ..... 52
4.1 Comparison between heterodyne and direct RF-sampling receivers [19] ..... 55
4.2 SEDE-HPF ..... 56
4.3 Reactive feedback/feedforward LNA ..... 56
4.4 Area-optimized RFFE, block level ..... 57
4.5 Layout of the area-optimized RFFE ..... 57
$4.6 \quad S_{11}$ of the area-optimized RFFE ..... 58
4.7 Real \& imaginary input impedance of the area-optimized RFFE ..... 58
$4.8 \quad S_{22}$ of the area-optimized RFFE ..... 59
4.9 Real \& imaginary output impedance of the area-optimized RFFE ..... 59
4.10 Voltage gain of the area-optimized RFFE ..... 60
4.11 Reverse isolation of the area-optimized RFFE ..... 61
$4.12 \mu$-factor for stability ..... 61
4.13 Noise figure for the area-optimized RFFE ..... 62
4.14 Large signal S-parameter, $S_{21}$, for area-optimized RFFE ..... 63
4.15 Full schematic of the BDFC-based front-end [6] ..... 65
4.16 PR-RFFE filter, PDPD-HPF from Chapter 2 ..... 67
4.17 PR-RFFE LNA branch for full-duplex mode ..... 68
4.18 PR-RFFE TX-driver stage ..... 68
4.19 PR-RFFE, block level ..... 69
4.20 Layout PR-RFFE ..... 70
$4.21 S_{11}$ of the PR-RFFE in HD-mode ..... 71
4.22 Real \& imaginary input impedances of the PR-RFFE in HD-mode ..... 71
$4.23 S_{22}$ of the PR-RFFE in HD-mode ..... 72
4.24 Real \& imaginary output impedances of the PR-RFFE in HD-mode ..... 72
4.25 Voltage gain of the PR-RFFE in HD-mode ..... 73
4.26 Reverse isolation of the PR-RFFE in HD-mode ..... 74
$4.27 \mu$-factor ..... 74
4.28 Noise figure for the PR-RFFE in HD-mode ..... 75
4.29 Large signal S-parameter, $S_{21}$, for PR-RFFE in HD-mode ..... 76
$4.30 S_{11}$ of the PR-RFFE in FD-mode ..... 77
4.31 Real \& imaginary input impedance of the PR-RFFE in FD- mode ..... 77
$4.32 S_{22}$ of the PR-RFFE in FD-mode ..... 78
4.33 Real \& imaginary output impedance of the PR-RFFE in FD-mode ..... 78
$4.34 S_{33}$ of the PR-RFFE in FD-mode ..... 79
4.35 Real \& imaginary input impedances on TX port of the PR- RFFE in FD-mode ..... 79
4.36 Voltage gain of the PR-RFFE in FD-mode ..... 80
4.37 Forward transmission gain, using TX as input port ..... 81
4.38 Reverse isolation of the area-optimized RFFE ..... 82
$4.39 \mu$-factor for stability ..... 82
4.40 Noise figure for the PR-RFFE in FD-mode ..... 83
4.41 Large signal S-parameter, $S_{21}$, PR-RFFE in FD-mode ..... 84
5.1 Microphotograph of the chip ..... 88
$5.2 S_{11}$ of the PR-RFFE in HD-mode ..... 89
5.3 Real \& imaginary input impedances of the PR-RFFE in HD-mode ..... 89
$5.4 S_{22}$ of the PR-RFFE in HD-mode ..... 90
5.5 Real \& imaginary output impedances of the PR-RFFE in HD-mode ..... 90
5.6 Forward voltage gain of the PR-RFFE in HD-mode ..... 91
5.7 Reverse isolation of the PR-RFFE in HD-mode ..... 92
$5.8 \mu$-factor ..... 92
5.9 Large signal S-parameter, $S_{21}$, for PR-RFFE in HD-mode ..... 93
$5.10 S_{11}$ of the PR-RFFE in FD-mode ..... 94
5.11 Real \& imaginary input impedance of the area-optimized RFFE. ..... 94
$5.12 S_{22}$ of the PR-RFFE in FD-mode ..... 95
5.13 Real \& imaginary output impedance of the PR-RFFE in FD-mode ..... 95
$5.14 S_{33}$ of the PR-RFFE in FD-mode ..... 96
5.15 Real \& imaginary input impedances on TX port of the PR- RFFE in FD-mode ..... 96
5.16 Forward voltage gain of the PR-RFFE in FD-mode ..... 97
5.17 Forward transmission gain, using TX as input port ..... 98
5.18 Reverse isolation of the area-optimized RFFE ..... 99
$5.19 \mu$-factor for stability ..... 99
5.20 Large signal S-parameter, $S_{21}$, PR-RFFE in FD-mode ..... 100
$5.21 S_{11}$ of the area-optimized RFFE. ..... 102
5.22 Real \& imaginary input impedance of the area-optimized RFFE. ..... 102
$5.23 S_{22}$ of the SEDERFFE. ..... 103
5.24 Real \& imaginary output impedance of the area-optimized RFFE. ..... 103
5.25 Forward voltage gain of the area-optimized RFFE ..... 104
5.26 Reverse isolation of the area-optimized RFFE ..... 105
$5.27 \mu$-factor for stability ..... 105
5.28 Large signal S-parameter, $S_{21}$, for area-optimized RFFE ..... 106

## List of Tables

2.1 Typical filter design specifications ..... 9
2.2 Passive filter network topologies. Derived from [7, 8]. ..... 10
2.3 Target specifications for the passive elliptic LC-filter with dual-harmonic traps ..... 13
2.4 Component parameters ..... 13
2.5 LC resonator parameters for the PDPD-filter ..... 14
2.6 Summary for the PDPD elliptical filter ..... 20
2.7 Target specifications for the passive elliptic LC-filter with a signle-harmonic trap ..... 21
2.8 Component parameters for the SEDE elliptic filter ..... 21
2.9 Component parameters of the transformer in the SEDE elliptic filter. ..... 22
2.10 LC resonator parameters for the SEDE-filter ..... 23
2.11 Summary for the SEDE elliptical filter ..... 30
2.12 Summary of the filter implementations. For filter specifications, see Tables 2.6 and 2.11 . ..... 31
3.1 Comparison between the two LNAs ..... 34
3.2 Passive component parameters of the LNA ..... 35
3.3 Active component dimensions of the LNA ..... 35
3.4 Trade-offs in terms of gain for the three common output stages. ..... 37
3.5 Summary of gain contributions in an LNA ..... 37
3.6 Summary of BW limitations in an LNA ..... 38
3.7 Summary of input impedance \& limitations ..... 40
3.8 Summary of noise contributions in the LNA. ..... 42
3.9 Biasing parameters derived using the $g m / I d$ method ..... 45
3.10 Design specifications of the LNA ..... 47
3.11 Summary of simulation results. ..... 53

## Chapter 1

## Introduction

Since it introduction in 2002, ultra-wideband (UWB) technology has gone through several metamorphosis stages. Fast forward to 2019, and with Apple's UWB U1 chip for communications the technology got a second lease on life [1]. Samsung's Galaxy Note 20 is the first Android smartphone to feature UWB [2]. Without doubt, UWB has gained a strong foothold in the commercial space.


Figure 1.1: Apple's U1 UWB chip for communications [1]

Today, UWB is primarily used for real-time location systems (RTLS) and radar systems. RTLS based on UWB RF communications provides location information (mainly indoor) when GPS is unable to do so. Decawave (now Qorvo) is still at the forefront of RTLS with their CMOS 90 nm DW1000 IC. As shown in Fig. 1.2, the transceiver is based on the classical zero-IF architecture.


Figure 1.2: Decawave's DW1000 IC for RTLS [3]

Novelda, on the other hand has shown how to use UWB technology for radar applications with their 55 nm CMOS X4 IC. This UWB human presence sensor (see figure below) measures vital signs, such as breathing, and will be integrated into Lenovo's flagship laptop, the ThinkPad X1 Nano [4].


Figure 1.3: Novelda's human presence sensor based on the X4 CMOS IC [4]

The 2-port, full-duplex transceiver is based on the direct-RF sampling architecture (see Fig. 1.4a). Direct-RF sampling reduces the complexity of RF front-ends (RFFE) and simplifies analog filtering requirements. The receiver bandwidth is from $6.1-10.5 \mathrm{GHz}$.


Figure 1.4: Block diagrams of the Novelda X4 IC [5]

In Fig. 1.4b, the high-pass filter removes interferers from the 5 GHz -band and is the $1^{\text {st }}$ block in the receiver chain. The circuit of the Q-boosted HPF is shown in Fig. 1.5. The filter employs parallel LC resonance in series with the RF signal path to block out-of-band interferers. Drawback is that the signal path and the interference blocking are not orthogonal. Thus, the insertion loss of the filter $>3 \mathrm{~dB}$.

We propose a double harmonic trap (DHT) HPF with series LC resonance (for out-of-band interferers) that is orthogonal to the RF signal path. This reduces the insertion loss to below 3 dB .


Figure 1.5: HPF with Q-enhancement [5]
For a single-ended antenna, we propose an area efficient single-ended to differential HPF with an 'embedded' on-chip balun. This is a single harmonic trap filter.


Figure 1.6: Wideband LNA [5]
In Fig. 1.6, the LNA comprise reactive feedforward (for passive voltage boosting) and negative feedback (for wideband impedance matching) loops via two asymmetrical uncoupled auto-transformers. The voltage at the
gate of the common-source stage is nearly $2 \times$ the input voltage. This $g_{m^{-}}$ boosted wideband LNA has a peak gain of 14.7 dB at 7.29 GHz .

We propose a current reuse reactively tuned pseudo-differential LNA based on Fig. 1.6 with an additional common-gate stage for higher reverse isolation.


Figure 1.7: Measured RF front-end gain of the HPF, LNA and preamplifier stages (buffers) [5]

We know that the full-duplex doubles the spectral capacity compared to half-duplex. Ferrite circulators employed for full-duplex mode are bulky and cannot be integrated on-chip. Magnetic-free circulators are becoming more and more attractive because of their small form factors and CMOS compatibility. For example, an active 2 -port full-duplex bi-directional frequency converter (BDFC) is shown in Fig. 1.8. It achieves +25 dB TX-ANT/ANT-RX isolation with an insertion loss of only 3 dB . One major drawback is its 48 mW power consumption.


Figure 1.8: Full schematic of the BDFC-based front-end [6]

## This work

Operating a differential 1-port UWB radar in full-duplex mode is most desirable as it would allow for single-antenna usage. This work presents two improved RF front-ends. Based on [5], the first of the two is a 1-port 'passively reconfigurable' RFFE, i.e. half-duplex operation in differential mode, and full-duplex operation in single-ended mode, see Fig. 1.9. Unlike [6], where the authors use 48 mA for FD-mode, we passively configure the RFFE from HD to FD.

The second design focuses on an area efficient single-ended to differential front-end. The area efficient HPF trades-off performance (insertion loss), yet the noise of the combined RFFE is $\ll 10 \mathrm{~dB}$.

HD-mode




## FD-mode



$$
\Gamma \Gamma^{\text {Tx-DRIVER }}-ー
$$

Figure 1.9: Block diagram of full-duplex single-ended RFFE

## Outline

In this thesis, we propose two passive high-pass filters (HPF) in Chapter 2, a wideband reactive low-noise amplifier (LNA) in Chapter 3, a 'passively reconfigurable' RF front-end (RFFE) and an area efficient RFFE comprising said components in Chapter 4. Measurement results are presented in Chapter 5. Conclusions are drawn in Chapter 6.

## Chapter 2

## High-Pass Filter Design

### 2.1 Introduction

In Table 2.1 we present a list of the specifications we'll deisgn and optimize for throughout this chapter.

Table 2.1: Typical filter design specifications

| Specification | Units |
| :---: | :---: |
| Pass-/Stop-band frequency | Hz |
| Passband ripple | dB |
| Stopband attenuation | dB |

Filters can be divided into 3 categories: active, passive and active plus passive (hybrid).
As the name suggests, active filters comprise active devices. Even with small form factors, very high frequency (UWB) RF front-ends rarely use 'purely' active filter because of the high power consumption and noise associated with them. $g m-C$ active filters using negative feedback OTAs are commonly used in narrowband receivers [7]. For example, high-Q band-pass or band-select $g m-C$ are used for selecting 20 MHz channels at 2.4 GHz .

Passive filters rely solely on passive components and are split in 4 groups: resistors \& capacitors (RC), resistors \& inductors (RL), inductors \& capacitors (LC) and resistors, inductors \& capacitors (RLC).
Series and parallel inductor-capacitor configurations offer frequency selectivity, and unlike resistors, do not add noise, thus making them attractive. However, inductors fall into the category of big body structures. Designers must find a compromise between die area and the (unloaded) Q-factor, with the latter directly affecting frequency selectivity, insertion loss and/or noise figure.
Hybrid filters can be defined as being either active or passive with complementary passive or active circuits for performance boosting. An example being an LC resonant tank with Q-enhancement/boosting via a cross-coupled pair.

Filter responses can be low-pass, high-pass, band-stop \& band-pass, or a combination of the aforementioned. In the congested RF spectrum, we must be able to clearly distinguish and select band(s) of interest. Thus, it is desirable to have a filter with a brick-wall like frequency response. As this is not physically realizable, it is paramount to choose a filter type with the steepest passband to stop-band transition.
For the unlicensed UWB spectrum stretching from 6-10 GHz, a band-pass or a high-pass filter [5] is the $1^{\text {st }}$ component in the receiver chain. As there are no known interferers above 10 GHz , we will design passive LC highpass filters in submicron CMOS with 1 ultra-thick-metal layer (UTM). Passive filter topologies to choose from are:

Table 2.2: Passive filter network topologies. Derived from $[7,8]$.

| Filter Topology | Characteristics |
| :---: | :--- |
| Butterworth | Passband response with maximal flatness |
| Chebyshev | Steeper transition from pass-to-stopband; larger phase <br> (w.r.t. Butterworth). Ripple in pass-band (type I) or <br> stop-band (type II). |
| Elliptic | Ripple in pass and stopband, transmission zeros; <br> steeper transition from pass-to-stopband; larger phase <br> distortion (w.r.t. Chebychev). |
| Bessel | Constant group delay (w.r.t. Butterworth); less steep <br> transition from pass-to-stopband (w.r.t. Chebyshev). |

### 2.2 Order

The order of the filter affects the stop-band attenuation, how steep the roll-off is, and the out-of-band \& in-band ripple of the filter response. Off-chip filters maximize the order to meet the specifications for stopband attenuation and roll-off. However on-chip inductors are big body structures, hence we're physically limited in terms of the order of the filter.
Once the order of the filter is chosen, we can improve out-of-band rejection by creating notches. Two adjacent notches can create a wideband notch ( $\geq 1 \mathrm{GHz}$ ). The notches are realized using parallel or series inductors and capacitors, which give us either an open or a short at the resonance frequency.
Keeping in mind the insertion loss and area, a $7^{\text {th }}$-order filter will be our upper limit. The minimum order is based on the requirements of the filter response itself. The required order of the filter will be a trade-off between stop-band atteunation \& roll-off, versus area.

For maximum roll-off and fastest transition between pass-band and stop-band, the elliptical topology will be used as a baseline for the filter implementation. The drawback with the elliptical topology is the in-band ripple as opposed to Chebychev type II, with only out-of-band ripple. However due to the broadband nature of the signal of interest we assume that the in-band ripple is negligible.

### 2.3 Center-frequency, cut-off frequency \& passband

The unlicensed UWB frequency spectrum strecthes from $3.1-10 \mathrm{GHz}$, we select a specific band withing this range. It can be subdivided into smaller bands, e.g. $3-5 \mathrm{GHz}$ and $6-10 \mathrm{GHz}$ to avoid 5 GHz interferers/blockers. For this work, we will be focusing on the $6-10 \mathrm{GHz}$ band.
Another factor that determines the center frequency is the frequency spectrum regulations. To be 'worldwide' compliant, a center frequency of 7.875 GHz is chosen.
The bandwidth is a systems level specification. Based on the transmitted pulse spectrum, the -10 dB -bandwidth is 1 GHz . the cut-off frequency \& the passband of the high-pass filter is derived from the system BW.

### 2.4 Specifications

Based on the previous discussions, we will decide the specifications for our passive LC HPF with harmonic traps.
As aforementioned, we want high stop-band attenuation and steep rolloff to attenuate most of the out-of-band. Dual harmonic traps create a wideband notch to supress the adjacent 5 GHz -band RF-blockers. As a trade-off between attenuation and area, it is desiablre to have a out-of-band attenuation $\geq 20 \mathrm{~dB}$ for frequencies below 5 GHz . To relax the requirements wrt. the order of the filter, an attenuation of 15 dB at 5.8 GHz is sufficient. The specification for attenuation is:

$$
\begin{align*}
\left|S_{21}\right|_{D H T} & =\left\{\begin{array}{l}
\geq 15 \mathrm{~dB}, f=5.8 \mathrm{GHz} \\
\geq 20 \mathrm{~dB}, f \leq 5.0 \mathrm{GHz}
\end{array}\right.  \tag{2.1}\\
\left|S_{21}\right|_{S H T} & =\left\{\begin{array}{l}
\geq 15 \mathrm{~dB}, f=5.8 \mathrm{GHz} \\
\geq 15 \mathrm{~dB}, f \leq 5.0 \mathrm{GHz}
\end{array}\right.
\end{align*}
$$

The -3 dB -passband is equal to the -10 dB -bandwidth, i.e. 1 GHz around the center frequency. We then have the filter passband defined as:

$$
\begin{equation*}
\text { Passband }=f_{c} \pm \frac{B W}{2}=7.875 \mathrm{GHz} \pm 500 \mathrm{MHz} \tag{2.2}
\end{equation*}
$$

This gives a passband in the range:

$$
\begin{equation*}
\text { Passband } \in[7.375 \mathrm{GHz}, 8.375 \mathrm{GHz}] \tag{2.3}
\end{equation*}
$$

where the lower boundary of 7.375 GHz sets the filter -3 dB cut-off frequency as well.
The maximum order is set to 7 . The minimum order is determined from the required stop-band attenuation, roll-off, cut-off frequency, and area. An LC filter synthesizer, sets our minimum order based on the design specifications to be 5 .
For maximum power transfer, we want to be impedance matched [9]. Given the passive LC filter implementation, one expects the filter to be of a transparent nature when impedance matched. Transparency lets the input impedance of the RF-receiver be determined by the suceeding block. The condition for being impedance matched is given by the maximum power transfer theorem, which states impedance matching when [9]:

$$
\begin{equation*}
Z_{\text {filter }}=Z_{a n t}^{*} \tag{2.4}
\end{equation*}
$$

where $Z_{\text {filter }}$ is the input impedance looking into the filter, and $Z_{a n t}^{*}$ is the complex conjugate of the characteristic impedance of the antenna. To quantify impedance matching, the reflected wave power should not be higher than $10 \%$ of the incident wave power [10]. In terms of S-parameters one is impedance matched under the condition:

$$
\begin{equation*}
S_{11} \leq-10 \mathrm{~dB} \tag{2.5}
\end{equation*}
$$

For $<1 \%$ power reflected, $S_{11} \leq-20 \mathrm{~dB}$. This our wanted specification. In terms of S-parameters, we define the NF \& IL requirement as:

$$
\mathrm{NF}=\mathrm{IL}=\left\{\begin{array}{l}
\left|S_{21}\right|_{D H T} \leq 3 \mathrm{~dB}  \tag{2.6}\\
\left|S_{21}\right|_{S H T} \leq 4 \mathrm{~dB}
\end{array}\right.
$$

### 2.5 DHT filter: pseudo-differential to pseudodifferential

Design specifications for the PD-PD DHT elliptic filter are shown in Table 2.3.
The series-first capacitance was removed due to it providing little to no performance gain, nor do we need DC isolation between the antenna and the filter, hence it was removed. The removal of this capacitance make the order of the filter to 4 .
The motivation behind the realization of a pseudo-differential to pseudodifferntial (PDPD) filter is to use the filter in a differential, as well as in a single-ended manner. This is a filter to be used for a pseudo full-duplex system, where we switch between single-ended and differential-mode. From Fig. 2.1, we see two notches, $C_{1}+L_{1} \& C_{3}+L_{2}$, in combination with the series capacitances $C_{2} \& C_{4}$. These reactances realizes a $4^{\text {th }}-$ order filter implementation.
The PDPD filter was built in a half-circuit manner, being symmetrical along the signal-path on both the positive and negative inputs. Looking at the half-circuit inductors, $L_{1} \& L_{2}$, we see that they're implemented

Table 2.3: Target specifications for the passive elliptic LC-filter with dualharmonic traps


Figure 2.1: Schematic for the PDPD-Filter
Table 2.4: Component parameters

| Name | Reactance | $Q_{f}{ }^{*}$ | SRF [GHz] |
| :---: | :---: | :---: | :---: |
| $C_{1}$ | 710 fF | 45.5 | 61.4 |
| $C_{2}$ | 470 fF | 67.1 | 76.3 |
| $C_{3}$ | 510 fF | 63 | 73.6 |
| $C_{4}$ | 1 pF | 32.5 | 51.3 |
| $L_{1}$ | 1.58 nH | 14.6 | 23.7 |
| $L_{2}$ | 1.58 nH | 14.6 | 23.7 |
| $* @ 5.8 \mathrm{GHz}$ |  |  |  |

as single-ended inductors. Using these single-ended inductors allows us to short one half of the circuit, without affecting the inductance on the other
half.
The inductors form a pseudo-differential topology. Realizing these inductors as center-tapped inductors as opposed to single-ended inductors would increase the common-mode rejection ratio (CMRR) of the filter.
The center-tapped inductor would trade-off the half-circuit functionality \& higher $Q_{f}$ for reduced area.
To attenuate the 5 GHz -band, we use a DHT filter. Notches are realized with series LC resonance. The two notches create a wideband notch. The two notches are realized by the reactances: $C_{1}+L_{1} \& C_{3}+L_{2}$.

Table 2.5: LC resonator parameters for the PDPD-filter

| Components | Notch frequency $[\mathrm{GHz}]$ | $Q_{f}{ }^{*}$ |
| :---: | :---: | :---: |
| $C_{1} \& L_{1}$ | 4.8 | 11.6 |
| $C_{3} \& L_{2}$ | 5.6 | 12 |
| ${ }^{*}$ a |  |  |

* @ notch frequency

The physical realization the schematic, Fig. 2.1, was done in the following manner:


Figure 2.2: Half-circuit layout for the PDPD-Filter
The PDPD is realized in a half-circuit manner, where the filter is symmetrical along the X-axis. The symmetry is done to maximize the CMRR of the filter.
UTM has the lowest sheet resistance, and is thus sued for all RF routing, which minimizes the insertion loss.
The passive structures were realized using TSMC's scalable symmetrical center tapped inductors (spiral_std_sym_ct_mu_z) \& scalable capacitors (crtmom_rf).

### 2.5.1 Simulation results

The input impedance matching $\left(S_{11}\right)$ of the SEDE filter, shows how well matched the filter is to $100 \Omega$ source. The worst case matching is in
the upper frequency of the passband, and is: $S_{11, \max }=-13.92 \mathrm{~dB}$ at 8.375 GHz.

The minimum required matched bandwidth is: $S_{11} \leq-10 \mathrm{~dB}$ in the range 6.1-9.3 GHz.

The recommended matched bandwidth is: $S_{11} \leq-20 \mathrm{~dB}$ in the range 6.57.7 GHz.

From $S_{11}$, we see that the matched bandwidth is skewed towards the lower frequencies, and not centered around the center frequency of 7.875 GHz .
This frequency offset is most likely due to additional parasitic capacitance.


Figure 2.3: $S_{11}$ for the PDPD elliptic filter.

The worst case output impedance matching $\left(S_{22}\right)$ is at the lower frequency of the passband, and is: $S_{22, \max }=-10.42 \mathrm{~dB}$ at 7.375 GHz . The minimum required matched bandwidth is: $S_{22} \leq-10 \mathrm{~dB}$ in the range $6.4-8.5 \mathrm{GHz}$. The recommended bandwidth, where the return loss is less or equal to $1 \%$ of the input signal, is not achieved for the output impedance matching. This filter is not to be used standalone, thus the output return loss is not of concern.
The IL \& NF was simulated by using a differential source of $100 \Omega$, while loading it with a differential load of $100 \Omega$. The worst case IL \& NF is shown to be in the lower frequency of the passband, i.e. at the cut-off frequency of the filter. The worst case scenario can be shown to be:

$$
\begin{align*}
I L_{\max } & =2.51 \mathrm{~dB}, \quad f=7.375 \mathrm{GHz} \\
N F_{\max } & =2.35 \mathrm{~dB}, \quad f=7.375 \mathrm{GHz} \tag{2.7}
\end{align*}
$$

In terms of bandwidth, we cover the IL \& NF requirements in a


Figure 2.4: Real input impedance of the PDPD elliptic filter.


Figure 2.5: Imaginary input impedance of the PDPD elliptic filter.
bandwidth of:

$$
\begin{array}{r}
I L \leq 3 \mathrm{~dB}, f \in[7,12] \mathrm{GHz} \\
N F \leq 3 \mathrm{~dB}, f \in[7,12] \mathrm{GHz} \tag{2.8}
\end{array}
$$

In the scenario of not being completely transparent, i.e. impedance


Figure 2.6: $S_{22}$ for the PDPD elliptic filter.


Figure 2.7: Real output impedance of PDPD elliptic filter.
mismatch, we expect that the IL is larger than the NF. Which is seen in the PDPD-filter realization.

Looking at the filter rejection, we find the attenuation at the bands of


Figure 2.8: Imaginary output impedance of PDPD elliptic filter.


Figure 2.9: Insertion loss \& NF from the PDPD elliptical filter.
interest to be:

$$
\text { Rejection }=\left\{\begin{array}{l}
\geq 14 \mathrm{~dB}, f \in[0,2.4] \mathrm{GHz},  \tag{2.9}\\
\geq 20 \mathrm{~dB}, f \in[3.8,5.4] \mathrm{GHz}, \\
17.32 \mathrm{~dB}, f=5.8 \mathrm{GHz}
\end{array}\right.
$$

While the 5 GHz band is sufficiently attenuated, the attenuation at 2.4 GHz is lower than the requirement. We expect the antenna to provide additional attenuation in the 2.4 GHz . From a systems level we expect sufficient attenuation for the 2.4 GHz band.


Figure 2.10: Transfer function of the PDPD elliptical filter.
The maximum group delay (GD) of the filter is in the lower frequency of the passband, and is $G D_{\max }=101.2 \mathrm{ps}$.
From the summary in Table 2.6, we see that the PDPD realization meets most of the design requirements.

### 2.6 SHT filter: single-ended to differential

Design specifications for the SE-DE SHT elliptic filter are shown in Table 2.7.
The series-first capacitance was removed due to it providing little to no performance gain, nor do we need DC isolation between the antenna and the filter, hence it was removed. The removal of this capacitance make the order of the filter to 4 .
The single-ended to differential filter (SEDE), is realized to provide an area-optimized version of the elliptical HPF. In addition to saving area on-chip, we want to save area off-chip by using a single-antenna.
From Fig. 2.12, we see a single notch, $C T-L_{1}+C_{2}$ in combination with the balun, $T_{1}$ and the series capacitances, $C_{1} \& C_{3}$. These reactances realizes a $4^{\text {th }}$-order filter.

The transformer $T_{1}$ is realized as a balun, which does the single-ended to


Figure 2.11: Group delay of the PDPD elliptical filter.

Table 2.6: Summary for the PDPD elliptical filter

| Parameter | Implementation | Specification |
| :---: | :---: | :---: |
| IL | $\leq 2.51 \mathrm{~dB}$ | $\leq 3 \mathrm{~dB}$ |
| NF | $\leq 2.36 \mathrm{~dB}$ | $\leq 3 \mathrm{~dB}$ |

RL:
Minimum ${ }^{*} \quad 6.1-9.3 \mathrm{GHz} \quad 7.375-8.375 \mathrm{GHz}$
Recommended $^{\dagger} \quad 6.5-7.7 \mathrm{GHz} \quad 7.375-8.375 \mathrm{GHz}$
$R L_{O}:$
Minimum $^{\ddagger} \quad 6.4-8.5 \mathrm{GHz} \quad 7.375-8.375 \mathrm{GHz}$
Recommended ${ }^{\S} \quad$ N/A $\quad 7.375-8.375 \mathrm{GHz}$
Rejection:

| @ 2.4 GHz | 14.59 dB | $\geq 20 \mathrm{~dB}$ |
| :---: | :---: | :---: |
| @ 5.0 GHz | 21.4 dB | $\geq 20 \mathrm{~dB}$ |
| @ 5.8 GHz | 17.32 dB | $\geq 15 \mathrm{~dB}$ |
| GD | $\leq 101.2 \mathrm{ps}$ | $\mathrm{N} / \mathrm{A}$ |

${ }^{*} S_{11} \leq-10 \mathrm{~dB}$
${ }^{\dagger} S_{11} \leq-20 \mathrm{~dB}$
$\ddagger S_{22} \leq-10 \mathrm{~dB}$
$\S S_{22} \leq-20 \mathrm{~dB}$
differential conversion through impedance transformation. We know that

Table 2.7: Target specifications for the passive elliptic LC-filter with a signle-harmonic trap


Figure 2.12: Schematic for the SEDE elliptic filter
Table 2.8: Component parameters for the SEDE elliptic filter

| Name | Reactance | $Q_{f}{ }^{*}$ | SRF $[\mathrm{GHz}]$ |
| :---: | :---: | :---: | :---: |
| $C_{1}$ | 350 fF | 88.1 | 89.1 |
| $C_{2}$ | 575 fF | 55.8 | 68.7 |
| $C_{3}$ | 1 pF | 32.5 | 51.3 |
| $A T_{1}{ }^{\dagger}$ | 2.7 nH | 20.1 | 17.9 |
| $*$ |  |  |  |

[^0]we can write the turns ratio between two windings [11]:
\[

$$
\begin{equation*}
\frac{n_{S}}{n_{P}}=\frac{v_{S}}{v_{P}}=\frac{i_{P}}{i_{S}}=\sqrt{\frac{L_{S}}{L_{P}}} \tag{2.10}
\end{equation*}
$$

\]

where $n_{S}$ is the number of turns in the secondary winding, $n_{P}$ is the number of turns in the primary winding, $v_{S}, v_{P}$ is the voltage across respectively the secondary and primary windings, $i_{P}, i_{s}$ is the current going through respectively the primary and secondary windings, and $L_{S}, L_{P}$ is the self-inductance of respectively the secondary winding, and the primary winding.
From Eq. (2.10), we find the required impedance transformation:

$$
\begin{equation*}
Z_{S}=\left(\frac{n_{S}}{n_{P}}\right)^{2} Z_{P}=\left(\frac{L_{S}}{L_{P}}\right) Z_{P} \tag{2.11}
\end{equation*}
$$

We know that our primary winding has an impedance equal to the characteristic impedance of the antenna, which we assume is:

$$
\begin{equation*}
Z_{P}=Z_{A n t}=50 \Omega \tag{2.12}
\end{equation*}
$$

Combining Eqs. (2.11) and (2.12), we can calculate the required turns ratio for the unbalanced to balanced tranformation of $1: \sqrt{2}$, or in terms of self-inductances, $L_{S}=2 L_{P}$.

Table 2.9: Component parameters of the transformer in the SEDE elliptic filter.

| Name | ${\text { Inductance }[\mathrm{nH}]^{*}}^{Q_{f}{ }^{*}}$ | SRF $[\mathrm{GHz}]$ | k |  |
| :---: | :---: | :---: | :---: | :---: |
| $T_{1}$ | $1.35 \mathrm{nH}^{\dagger}$ | 1.12 | 10.67 | 0.82 |
| $T_{1-P}$ | 0.44 nH | 4.25 | $>20$ | N/A |
| $T_{1-S}$ | $0.75 \mathrm{nH}^{\S}$ | 4.50 | $>20$ | N/A |

* @ 7.875 GHz
$\dagger$ Mutual inductance between the two windings
§ Differentially driven

To minimize the on-chip area, a single harmonic trap is realized, as opposed to dual harmonic trap. The notch is realized through reactances, $C_{2}$ in series with the center-tapped inductor, $C T-L_{1}$. This SHT resonates at 5.7 GHz ,i.e. at the upper frequency of the 5 GHz -band. In the SEDE-filter there's one additional resonator, and that's the series connection between the secondary winding of the balun, $T_{1-S}$ and the series capacitance, $C_{1}$. To minimize insertion loss, this series resonator resonates withing the passband.
To minimize area, the notch inductor is realized using a center-tapped inductor instead of a single-ended inductor. The center-tapped inductor has a higher self-inductance due to the positive mutual inductance,

Table 2.10: LC resonator parameters for the SEDE-filter

| Components | Resonance frequency [GHz] | ${Q_{f}{ }^{*}}^{T_{1-S}+C_{1}{ }^{\dagger}} \quad 6.7$ |
| :---: | :---: | :---: |
| $C_{2}+A T_{1}{ }^{\S}$ | 5.7 | 3.8 |

* @ resonance frequency
$\dagger$ With the center-tap as input, single-ended
§ Differentially driven
when driven differentially. Assuming perfect coupling between the two windings, we estimate the self-inductance of the transfomer to be $\approx 4 \times$ the inductance of a single-ended equivalent (in area) [11]. In addition, we also get an increase of $Q_{f}$ when driven differentially [11].


Figure 2.13: Half-circuit layout for the SEDE-Filter

The SEDE is realized in a half-circuit manner, where the filter is symmetrical along the X-axis. The symmetry is done to maximize the CMRR of the filter.
UTM has the lowest sheet resistance, and is thus sued for all RF routing, which minimizes the insertion loss.
The passive structures were realized using TSMC's scalable symmetrical center tapped inductors (spiral_std_sym_ct_mu_z) \& scalable capacitors (crtmom_rf).
The balun is implemented using the foundry model for the inductor, which
is equivalent to $C T-L_{1}$ (see Fig. 2.12), as the secondary winding. For the primary winding we use the AP-redistribution layer which sits on top of the UTM. The stacked transformer topology is used to maximize the coupling coefficient between the primary and secondary winding [11], which in turn decreases the insertion loss of the transformer. The primary winding was offset by a distance, $d$, to reduce the amount of coupling capacitance between the two windings, which in turn increase the selfresonance frequency of the balun [11].


Figure 2.14: Transformer $T_{1}$ from Fig. 2.12

### 2.6.1 Simulation results

The input impedance matching $\left(S_{11}\right)$ of the SEDE filter, shows how well matched the filter is to $50 \Omega$ source. The worst case matching is in the upper frequency of the passband, and is: $S_{11, \max }=-19.74 \mathrm{~dB}$ at 8.375 GHz .

The minimum required matched bandwidth is: $S_{11} \leq-10 \mathrm{~dB}$ in the range $6.4-12 \mathrm{GHz}$.

The recommended matched bandwidth is: $S_{11} \leq-20 \mathrm{~dB}$ in the range 6.98.3 GHz.

From $S_{11}$, we see that the matched bandwidth is skewed towards the lower frequencies, and not centered around the center frequency of 7.875 GHz .
This frequency offset is most likely due to additional parasitic capacitance.


Figure 2.15: $S_{11}$ for the SEDE elliptic filter.

The worst case output impedance matching $\left(S_{22}\right)$ is at the lower frequency of the passband, and is: $S_{22}=-13.64 \mathrm{~dB}$ at 7.375 GHz .

The minimum required matched bandwidth is: $S_{22} \leq-10 \mathrm{~dB}$ in the range $7-10.8 \mathrm{GHz}$.

The recommended matched bandwidth is: $S_{22} \leq-20 \mathrm{~dB}$ in the range 89 GHz .

This filter is not to be used standalone, thus the output return loss is not of concern

The IL \& NF was simulated with a single-ended $50 \Omega$ source, and a $100 \Omega$ differential load. The worst case IL \& NF is shown to be in the lower frequency of the passband, i.e. at the cut-off frequency of the filter. The worst case scenario is:

$$
\begin{align*}
I L_{\max } & =3.06 \mathrm{~dB}, \quad f=7.375 \mathrm{GHz} \\
N F_{\max } & =2.95 \mathrm{~dB}, \quad f=7.375 \mathrm{GHz} \tag{2.13}
\end{align*}
$$



Figure 2.16: Real input impedance of the SEDE elliptic filter.


Figure 2.17: Imaginary input impedance of the SEDE elliptic filter.

We cover the IL/NF specifications over the following bands:

$$
\begin{align*}
I L & \leq 3 \mathrm{~dB}, f \in[7.5,10.3] \mathrm{GHz} \\
N F & \leq 3 \mathrm{~dB}, f \in[7.4,12] \mathrm{GHz} \tag{2.14}
\end{align*}
$$



Figure 2.18: $S_{22}$ for the SEDE elliptic filter.


Figure 2.19: Real output impedance of SEDE elliptic filter.

If we compare the $N F_{\text {max }}$ for the SEDE vs PDPD (see Eq. (2.7)), the SEDE filter has 0.7 dB higher NF. This added NF is due to the non-ideal coupling between the primary and secondary windings. The insertion loss


Figure 2.20: Imaginary output impedance of SEDE elliptic filter.
is approximated as:

$$
\begin{equation*}
I L_{k}=\left|10 \log _{10}(|k|)\right|=\left|10 \log _{10}(|0.82|)\right| \approx 0.86 \mathrm{~dB} \tag{2.15}
\end{equation*}
$$

where k is the coupling coefficient between the primary and secondary winding in the balun (see Table 2.9).
Attenuation at the bands of interest are:

$$
\text { Rejection }=\left\{\begin{array}{l}
\leq 24 \mathrm{~dB}, f \in[0,2.4] \mathrm{GHz}  \tag{2.16}\\
\leq 15 \mathrm{~dB}, f \in[0,5.9] \mathrm{GHz} \\
17.3 \mathrm{~dB}, f=5.8 \mathrm{GHz}
\end{array}\right.
$$

The 2.4 GHz -band, and the upper frequency of the 5 GHz is sufficiently attenuated. However, between the 2.4 GHz - and 5 GHz -band is lower than the specifications. If we compare the results with the ones from the PDPD filter (see Eq. (2.9)), we see that an additional notch provides the required attenuation at frequencies below the 5 GHz -band.
The maximum group delay (GD) of the filter is in the lower frequency of the passband, and is $G D_{\max }=115.06 \mathrm{ps}$.
From Table 2.11, we see that the SEDE implementation the design specifications.

### 2.7 Conclusion

In this chapter we've gone over the fundamentals of filters, set design specifications, realized two different elliptical passive LC filters.


Figure 2.21: Insertion loss \& NF from the SEDE elliptical filter.


Figure 2.22: Transfer function of the SEDE elliptical filter.

The two filters match the design specification, and hence can be used in a receiver RF front-end. The pseudo-differential to pseudo-differential filter elliptical filter is used in a pseudo simultaneous transmit and receive RF receive, while the single-ended to differential elliptical filter is used in a single-antenna, single-ended to differential area optimized RF receiver


Figure 2.23: Group delay of the SEDE elliptical filter
Table 2.11: Summary for the SEDE elliptical filter

| Parameter | Implementation | Specification |
| :---: | :---: | :---: |
| IL | $\leq 3.06 \mathrm{~dB}$ | $\leq 4 \mathrm{~dB}$ |
| NF | $\leq 2.95 \mathrm{~dB}$ | $\leq 4 \mathrm{~dB}$ |
| $\begin{gathered} S_{11}: \\ \text { Minimum } \\ \text { Recommended }{ }^{\dagger} \end{gathered}$ | $\begin{aligned} & 6.4-12 \mathrm{GHz} \\ & 6.9-8.3 \mathrm{GHz} \end{aligned}$ | $\begin{aligned} & 7.375-8.375 \mathrm{GHz} \\ & 7.375-8.375 \mathrm{GHz} \end{aligned}$ |
| $\begin{gathered} S_{22} \\ \text { Minimum } \\ \text { Recommended }^{\ddagger} \end{gathered}$ | $\begin{gathered} 7-10.8 \mathrm{GHz} \\ 8-9 \mathrm{GHz} \end{gathered}$ | $\begin{aligned} & 7.375-8.375 \mathrm{GHz} \\ & 7.375-8.375 \mathrm{GHz} \end{aligned}$ |
| Rejection: <br> @ 2.4 GHz <br> @ 5.0 GHz <br> @ 5.8 GHz | $\begin{aligned} & 23.93 \mathrm{~dB} \\ & 16.69 \mathrm{~dB} \\ & 17.30 \mathrm{~dB} \end{aligned}$ | $\begin{aligned} & \geq 15 \mathrm{~dB} \\ & \geq 15 \mathrm{~dB} \\ & \geq 15 \mathrm{~dB} \end{aligned}$ |
| GD | $\leq 115.06 \mathrm{ps}$ | N/A |

${ }^{*} S_{11} \leq-10 \mathrm{~dB}$
$\dagger$
$S_{11} \leq-20 \mathrm{~dB}$
$\ddagger$
$S_{22} \leq-10 \mathrm{~dB}$
${ }^{\S} S_{22} \leq-20 \mathrm{~dB}$
front-end.

Table 2.12: Summary of the filter implementations. For filter specifications, see Tables 2.6 and 2.11.

| Parameter | PDPD | SEDE |
| :---: | :---: | :---: |
| IL | $\leq 2.51 \mathrm{~dB}$ | $\leq 3.06 \mathrm{~dB}$ |
| NF | $\leq 2.36 \mathrm{~dB}$ | $\leq 2.95 \mathrm{~dB}$ |
| $S_{11}:$ |  |  |
| Minimum $^{*}$ | $6.1-9.3 \mathrm{GHz}$ | $6-9.2 \mathrm{GHz}$ |
| Recommended $^{\dagger}$ | $6.5-7.7 \mathrm{GHz}$ | $6.9-8.3 \mathrm{GHz}$ |
| $S_{22}:^{\ddagger}$ |  |  |
| Minimum $^{\ddagger}$ | $6.4-8.5 \mathrm{GHz}$ | $6.3-8.6 \mathrm{GHz}$ |
| Recommended $^{\S}$ | $\mathrm{N} / \mathrm{A}$ | $8-9 \mathrm{GHz}$ |
| Rejection: $^{\text {@ } 2.4 \mathrm{GHz}}$ | 14.59 dB | 23.93 dB |
| @ 5.0 GHz | 21.4 dB | 16.69 dB |
| @ 5.8 GHz | 17.32 dB | 17.30 dB |
| GD | $\leq 101.2 \mathrm{ps}$ | $\leq 115.1 \mathrm{ps}$ |

${ }^{*} S_{11} \leq-10 \mathrm{~dB}$
$\dagger S_{11} \leq-20 \mathrm{~dB}$
$\ddagger S_{22} \leq-10 \mathrm{~dB}$
$\S S_{22} \leq-20 \mathrm{~dB}$

## Chapter 3

## Low-Noise Amplifier Design

### 3.1 Introduction

The low-noise amplifier (LNA) is a crucial block in the receiver chain. Its primary goal is to amplify the received (or preferably the wanted) signal to an adequate level without significantly elevating the noise floor. Its secondary purpose is to suppress 'noise sources' from all succeeding blocks along the receiver chain, such as mixers found in homo- or heterodyne architectures. So, higher the gain of the LNA, the lower the noise contributions of subsequent circuits.
If the LNA is the first block of the receiver chain, its input impedance must be matched to that of the antenna for maximum power transfer. As we have seen in Chapter 2, the HPF is the first block in our receiver to suppress interferers from the 5 GHz band which will most likely saturate the LNA. Thus, the input impedance of the LNA is transformed via the HPF. This allows the designer a greater degree of flexibility in choosing the input impedance of the LNA. In our case, the HPF components are scaled for impedances of 100 Ohms, which is also the differential impedance of the antenna.
In this chapter, we will start by going over the basics of LNA design, briefly look at classical topologies, and then provide circuit specifications for our design.

### 3.2 Circuit design

The LNA circuit is based on Novelda's UWB Radar SoC's LNA [5]. The two LNAs comprise cascode (common source plus common gate stage) input stages. This current reuse LNA adds an extra CG-stage 'on top' for improved reverse isolation. This, however, requires a power supply $>1 \mathrm{~V}$. The feedforward and feedback reactive loops via on-chip symmetrical center-tapped transformers provide passive voltage boosting and impedance matching, respectively.
Table 3.1 compares the LNA in [5] and this work. For higher voltage headroom, we scrap the current source. For better common-mode rejection, we replace the single-ended spiral inductors in [5] with a single
differential center-tapped inductor.
Table 3.1: Comparison between the two LNAs

|  | Novelda [5] | Implementation |
| :--- | :--- | :--- |
| Input stage | CS-stage with passive <br> voltage boosting | CS-stage with passive <br> voltage boosting |
| Active stages | CS+CG | CS+CG+CG |
| Input/output | Differential to pseudo- <br> differential | Pseudo-differential to <br> differential |
| Signal-conditioning | In-band peaking with <br> bridged T-coil | In-band peaking with <br> center-tapped LC-tank |



Figure 3.1: The LNA, including biasing for the input stage, and output buffers.

Table 3.2: Passive component parameters of the LNA.

| Name | Value | Q-value ${ }^{*}$ | SRF [GHz] |
| :--- | :---: | :---: | :---: |
| $A T_{1}{ }^{\dagger}$ | 1.05 nH | 9.8 | $>20$ |
| $A T_{2}{ }^{\dagger}$ | 2.1 nH | 9.2 | 18.3 |
| $A T_{3}{ }^{\dagger}$ | 1.7 nH | 13.6 | $>20$ |
| $C_{C}$ | 510 fF | 46.2 | $>20$ |
| $C_{D} \ddagger$ | 6.8 pF | $\mathrm{N} / \mathrm{A}$ | $\mathrm{N} / \mathrm{A}$ |
| $C_{3}$ | 165 fF | 126 | $>20$ |
| $R_{1}$ | $5.1 \mathrm{k} \Omega$ | $\mathrm{N} / \mathrm{A}$ | $\mathrm{N} / \mathrm{A}$ |
| $R_{2}$ | $2.2 \mathrm{k} \Omega$ | $\mathrm{N} / \mathrm{A}$ | $\mathrm{N} / \mathrm{A}$ |
| ${ }^{*}$ @ $f_{c}=7.875 \mathrm{GHz}$ |  |  |  |
| $\dagger$ <br> Measured while being differentially <br> driven. <br> $\ddagger$ <br> Implemented through the usage of an <br> inverter. |  |  |  |

Table 3.3: Active component dimensions of the LNA

| Name | Ratio $[\mu \mathrm{m} / \mathrm{nm}]$ |
| :---: | :---: |
| $M_{1}$ | $110 / 80$ |
| $M_{2}$ | $110 / 70$ |
| $M_{3}$ | $110 / 70$ |
| $M_{C D}$ | $40 / 60$ |

Typical design specifications we use for designing LNAs:

| Parameter | Unit |
| :---: | :---: |
| Gain | dB |
| Bandwidth | GHz |
| Noise Figure | dB |
| Reverse Isolation | dB |
| 1-dB compression point | dBm |
| Impedance matching | dB |
| Power consumption | mW |

In terms of gain of the active stage, there's no difference between whether
going for a CS-stage or a CG-stage ${ }^{1}$. We know that the gain of a single stage amplifier can be written as:

$$
\begin{equation*}
A_{V}=\frac{v_{\text {out }}}{v_{\text {in }}}=\left|g_{m} r_{o}\right| \tag{3.1}
\end{equation*}
$$

where the $g_{m}$ is the transconductance of the MOSFET, and $r_{o}$ is the drain-source resistance due to the channel length modulation. The resistance can be approximated to [12]:

$$
\begin{equation*}
r_{o} \approx \frac{1}{\lambda I_{D}} \tag{3.2}
\end{equation*}
$$

where $\lambda$ is the 'channel-length modulation coefficient', which scales with $\lambda \propto \frac{1}{L}$. While one can scale the intrinsic gain up by maximizing the current (increasing $g_{m}$ ) and make the device longer (increasing $r_{o}$ ), there is an upper limit for intrinsic gain. For a 65 nm process this is approximately 10 . To increase the gain, one typically loads the active device with an additional resistive load, however due to the system requirement of having signal conditioning, a resistive load is not viable. To achieve a higher gain, without having to be dependent on the physical length of the device itself, we use a tuned load (LC-tank). The gain of an LC-tank can be approximated to [13]:

$$
\begin{equation*}
A_{V}=g_{m} \cdot\left(R_{P} \| r_{o}\right) \approx g_{m} \cdot \omega L Q=g_{m} Q^{2} r_{s}, R_{P} \gg r_{o} \tag{3.3}
\end{equation*}
$$

at the resonance frequency. The benefits of using a tuned amplifier are:

- Voltage drop: Ideally the voltage drop across the LC tank is 0, however due to the metals itself in the inductor, there will always be some resistance $\left(r_{s}\right)$. This resistance is small when compared the resistive load, hence the voltage drop is significantly less.
- Voltage swing: it's possible to achieve an AC voltage swing across the LC tank higher than the DC-voltage [13].
- Bandwidth selectivity: If the tank is tuned correctly we will achieve a bandpass response. This provides additional filtering with regards to out-of-band blockers.

On the other hand, on-chip inductors are bulky (in terms of area), and lossy. One expect a maximum $Q \approx 20$.

[^1]Table 3.4: Trade-offs in terms of gain for the three common output stages.

| Parameter | Unloaded | Resistive | Tuned |
| :---: | :---: | :---: | :---: |
| Gain | Low | High | Q-dependent |
| Voltage drop | None | High | Low |
| Peak voltage | $V_{D D}$ | $V_{D D}$ | $>V_{D D}{ }^{*}$ |
| Area | Very low | Low | High |

* Swings around the DC-level.

For $A_{V} \geq 30 \mathrm{~dB}$ and frequency selectivity, we will use an LC-tank as the LNA load.

Table 3.5: Summary of gain contributions in an LNA

| Component | Gain | Limitation |
| :---: | :---: | :---: |
| Unloaded | $g_{m} r_{o}$ | $W / L \&$ process dependent. |
| Resistive load | $g_{m} R_{L}$ | Noise, DC voltage drop. |
| Tuned load | $g_{m} \omega L Q$ | Area \& $Q_{f}{ }^{\dagger}$ |

${ }^{\dagger}$ Dependent on the available material of the process.
The center frequency for the LNA is given by the resonant frequency of the LC-tank as:

$$
\begin{equation*}
f_{c}=\frac{1}{2 \pi \cdot \sqrt{L C}} \tag{3.4}
\end{equation*}
$$

where $L$ is the self-inductance of the inductor, and C is the capacitance (including parasitic capacitance). To maximize gain, we will find the value of $L$ with the highest value of Q . Thus, $L_{A T_{3}}=1.7 \mathrm{nH}$ and $C_{3}=165 \mathrm{fF}$, and the resonant frequency is:

$$
\begin{equation*}
f_{c}=\frac{1}{2 \pi \sqrt{1.7 \mathrm{nH} \cdot 165 \mathrm{fF}}} \approx 9.5 \mathrm{GHz} \tag{3.5}
\end{equation*}
$$

If we wanted to match the system center frequency, $f_{c}=7.875 \mathrm{GHz}$, we have to see a parasitic capacitance value of $C_{p a r} \approx 75 \mathrm{fF}$. This parasitic capacitance is mostly dependent on the layout itself, and especially the width and length of the signal-wire itself. Moving onward, we assume that the resonant frequency is at the center frequency of the system, $f_{c}=7.875 \mathrm{GHz}$.
In terms of bandwidth, we have that the device bandwidth, i.e. the maximum frequency of operation on the device level itself to be:

$$
\begin{equation*}
B W=f_{T} \approx \frac{g_{m}}{2 \pi C_{g g}} \tag{3.6}
\end{equation*}
$$

where $f_{T}$ is the transit frequency of the device, $C_{g g}$ is the total capacitance looking into the gate, and $g_{m}$ is the transconductance. While
the transit frequency doesn't show exactly the 'practical' bandwidth, it gives us the upper frequency of operation. For the 65 nm we know the transit frequency to be much larger than the frequency of interest. The effective bandwidth is limited to the output stage. In the case of using a tuned load, we know that the fractional -3 dB bandwidth can be written as [13]:

$$
\begin{equation*}
B W=f_{r}=\frac{f_{c}}{Q} \tag{3.7}
\end{equation*}
$$

where $f_{r}$ is the fractional bandwidth, $f_{c}$ is the center frequency, and Q is the quality factor of the tank.
For the bandwidth specification, we have the system level requirement of:

$$
\begin{equation*}
B W_{-10 \mathrm{~dB}}=1 \mathrm{GHz}, \quad f_{c} \pm 500 \mathrm{MHz} \tag{3.8}
\end{equation*}
$$

To find the $Q$, we use the -3 dB -bandwidth, and find that to be:

$$
\begin{equation*}
Q_{f}=\frac{f_{c}}{B W_{-3 \mathrm{~dB}}}=\frac{7.875 \mathrm{GHz}}{400 \mathrm{MHz}} \approx 19.7 \tag{3.9}
\end{equation*}
$$

This gives us a $Q$ of 19.7 , which in turn gives us a -10 dB BW of 1 GHz . If required, one could extend the effective bandwidth implementing $T$-coil bandwidth enhancement $[13,14]$.

Table 3.6: Summary of BW limitations in an LNA

| Component | BW | Limitation |
| :---: | :---: | :---: |
| No ext. load | $\frac{g_{m}}{2 \pi C_{g g}}$ | $W L$ \& process dependent. |
| Resistive load | $\frac{g_{m}}{2 \pi R C_{g g}}$ | $R \& f_{t}{ }^{*}$. |
| Tuned load | $\frac{f_{c}}{Q_{f}}$ | $f_{c} \& Q_{f}{ }^{\dagger}$ |

* Dependent on the input stage.
$\dagger$ Dependent on the available material of the process.

To ensure that the characteristics of the LC-tank doesn't change, we need to ensure that the load of the tank is constant. This can be achieved through the usage of common-drain (CD) stages, where we do the voltage buffering between the LC-tank and the 'outside world'. The implementation of the CD-stages is shown in Fig. 3.1, where they are dimensioned to achieve an output impedance of:

$$
\begin{equation*}
Z_{o u t}=\frac{1}{g_{m}}=50 \Omega \tag{3.10}
\end{equation*}
$$

where we assumed that the output load is $Z_{\text {out }}=50 \Omega$. For the output matching, it's the same as with the CG-stage, the $g_{m}$ is set to be the reciprocal of the load, i.e. $g_{m}=50 \Omega^{-1}=25 \mathrm{mS}$.
For the CD-stage we trade-off impedance isolation for power $\&$ gain.

For the input impedance matching, we're dependent on the realized input stage, as well as the input matching network. A CG-stage has an input impedance of [15]:

$$
\begin{equation*}
R_{i n} \approx \frac{1}{g_{m}} \tag{3.11}
\end{equation*}
$$

For an inductively loaded CS-stage, we have the input impedance as [15]:

$$
\begin{equation*}
Z_{i n}(s)=\frac{L_{1}\left(C_{1}+C_{f}\right) s^{2}+R_{s}\left(C_{1}+C_{f}\right) s+1}{\left[L_{1} C_{1} s^{2}+\left(R_{s} C_{1}+g_{m} L_{1}\right) s+1+g_{m} R_{s}\right] C_{f} s}, \tag{3.12}
\end{equation*}
$$

where we have the output inductor, $L_{1}$, the output capacitance, $C_{1}$, the feedback capacitance between the gate and the drain of the CS-stage, $C_{F}$, the parasitic series resistance in the output inductor, $R_{S}$, and $g_{m}$ is the transconductance of the CS-stage. One of the issues with a single CSstage is that the feedback capacitor, $C_{f}$ creates a negative resistance at a certain frequency.
While CG-stage is easily matched when compared to the CS-stage, we want to be able to increase the $g_{m}$ of the device (to increase gain) without sacrificing input impedance matching. Moving forward, a CS-stage is the chosen topology for the input stage.
To do impedance matching on the CS-stage, we want to resonate out both the capacitance seen between gate-drain, as well as gate-source. Using inductive degeneration, we manage to resonate the gate-source capacitance out. The inductive degeneration is implemented through the auto-transformer, $A T_{1}$ (Fig. 3.1). To resonate out the gate-drain capacitance, we will be using a series inductance, $A T_{2}$ (Fig. 3.1). If we couple these two auto-transformers, there are several benefits: the input impedance matching, is the impedance transformed by the coupling of the two windings, created by $A T_{1} \& A T_{2}$. For transfomers, we have the general impedance tranformation:

$$
\begin{equation*}
Z_{p}=\frac{Z_{s}}{N^{2}} \tag{3.13}
\end{equation*}
$$

where, $Z_{p}$ is the impedance looking into the first winding, and $Z_{s}$ is the impedance looking into the secondary winding, while $N$ is the turn ratio between secondary and primary turn ratio. In the case of looking into the source-side of a MOSFET we have the relation:

$$
\begin{equation*}
Z_{i n}=Z_{p}=\frac{\frac{1}{g_{m}}}{N^{2}} \tag{3.14}
\end{equation*}
$$

By coupling the current back into the gate in the positive feedback loop, we achieve passive $g_{m}$-boosting [16]. The effective $g_{m}$-boosted transconductance, $G_{m}$ can be written as:

$$
\begin{equation*}
G_{m}=k \cdot \frac{N_{S}}{N_{P}} \cdot g_{m} \tag{3.15}
\end{equation*}
$$

Thus, the input impedance looking into the $g_{m}$-boosted CS-stage, assuming we're at resonating out all the capacitance:

$$
\begin{equation*}
Z_{i n}=\frac{\frac{1}{G_{m}}}{N_{P, S}^{2}}=\frac{\frac{1}{\frac{N_{S}}{N_{P}} \cdot g_{m}}}{\left(\frac{N_{P}}{N_{S}}\right)^{2}}=\frac{N_{S}}{N_{P}} \cdot \frac{1}{g_{m}} \tag{3.16}
\end{equation*}
$$

which in terms of self-inductance can be written as [11]:

$$
\begin{equation*}
Z_{i n}=\frac{N_{S}}{N_{P}} \cdot \frac{1}{g_{m}}=\frac{\sqrt{L_{2} L_{1}}}{g_{m}} \tag{3.17}
\end{equation*}
$$

From a matching perspective we're now contrained by the transconductance of the device, as well as the turn ratio between the autotransformers, acting as the primary and secondary windings.
While the turn ratio between the two windings provides a higher effective transconductance we're somewhat limited by the size of the device. We will use a $2: 1$ self-inductance ratio between the secondary and the primary winding. To find the required $g_{m}$ of the input stage, we need to solve Eq. (3.17) for $g_{m}$. For $Z_{i n}=50 \Omega$, and a self-inductance ratio of $2: 1$, we get:

$$
\begin{equation*}
g_{m}=\frac{\sqrt{L_{2} L_{1}}}{Z_{i n}}=\frac{\sqrt{2}}{50 \Omega} \approx 28.28 \mathrm{mS} \tag{3.18}
\end{equation*}
$$

Table 3.7: Summary of input impedance \& limitations

| Component | $Z_{i n}$ | Limitation |
| :---: | :---: | :---: |
| CG-stage | $\frac{1}{g_{m}}$ | $\mathrm{~N} / \mathrm{A}$ |
| CS-stage | See Eq. $(3.12)$ | Load, $C_{F} \& g_{m}$ |
| CS-stage $^{*}$ | $\frac{\sqrt{L_{2} L_{1}}}{g_{m}}$ | $N_{S, P^{\dagger}}$ |

* With passive voltage boosting
$\dagger$ Turn ratio between secondary- and primary winding.

To maximimize the SNR of the system, we need the LNA to contribute as much gain as possible, while adding the least amount of possible noise to the system. With regards to noise figure (NF), we care about the added noise from the input stage CS-/CG- as well as from the output stage.
For an input impedance matched CG-input stage, i.e. $g_{m}=\frac{1}{R_{S}}$, we can estimate the NF to be [15]:

$$
\begin{align*}
N F & =1+\frac{\gamma}{g_{m} R_{S}}+\frac{R_{S}}{R_{1}}\left(1+\frac{1}{g_{m} R_{S}}\right)^{2}  \tag{3.19}\\
& =1+\gamma+4 \frac{R_{S}}{R_{1}}
\end{align*}
$$

where $\gamma$ is the process dependent constant, $R_{S}=50 \Omega$ is the source resistance and $R_{1}$ is the load resistance.
Assuming that $\gamma=1$, and that $4 \frac{R_{S}}{R_{1}} \ll 1+\gamma$, we get an NF of 3 dB . A lower NF is achievable at the cost of impedance matching.
For a CS-stage, we can estimate the NF to be [15]:

$$
\begin{align*}
N F & =\frac{4 k T \gamma g_{m} r_{o}^{2}+4 K T R_{S}\left(g_{m} R_{o}\right)^{2}}{\left(g_{m} R_{o}\right)^{2}} \cdot \frac{1}{4 k T R_{S}}  \tag{3.20}\\
& =\frac{\gamma}{g_{m} R_{S}}+1
\end{align*}
$$

where $\gamma$ is a process dependent constant, $g_{m}$ is the transconductance of the CS-stage, and $R_{S}$ is the source resistance.
For a CS-stage we can achieve a better noise performance by increasing the transconductance of the device, at the cost of increased power consumption.
In addition to the active input stage, the output stage also contributes additional noise. For an LC-tank, the parastic series resistance in the tank adds thermal noise to the total noise power. We can define the added noise from an LC-tank as:

$$
\begin{equation*}
\overline{V_{n}^{2}}=4 k T R_{S}=4 k T \frac{\omega L}{Q} \tag{3.21}
\end{equation*}
$$

Due to the small series resistance in the LC-tank, we assume that the input stage itself is the dominant noise contributor. In terms of specifications we want a total $\mathrm{NF} \leq 3 \mathrm{~dB}$.
Solving Eq. (3.20) for $g_{m}$, the required transconductance of the CS-stage required to achieve the specification is:

$$
\begin{equation*}
g_{m}=\frac{\gamma}{2 R_{S}}=\frac{1}{2 \cdot 50 \Omega}=25 \mathrm{mS} \tag{3.22}
\end{equation*}
$$

where $\gamma=1$, and $R_{S}=50 \Omega$. In terms of $g_{m}$, one can trade-off noise \& gain for area and/or power consumption.
Reverse isolation is the 'reverse gain' of the device. For zero-/low-IF receivers, a high reverse isolation of the LNA is required to minimize LOleakage to the antenna. An amplifier with low reverse isolation is prone to instability because of the positive feedback through parasitic paths from the output to the input. For high reverse isolation, one employs cascoding. The reverse gain for a cascode is [15]:

$$
\begin{equation*}
\frac{V_{a n t}}{V_{x}} \approx \frac{s R_{a n t} C_{g d}}{g_{m 2} r_{o 2}} \tag{3.23}
\end{equation*}
$$

From Eq. (3.23), we see that the reverse isolation increase by a factor $g_{m} r_{o}$. We trade off added reverse isolation for voltage headroom. The CGstage reduce the Miller capacitance.

Table 3.8: Summary of noise contributions in the LNA.

| Component | Type of noise | Limitation |
| :---: | :---: | :---: |
| MOSFETs | Thermal | $\gamma$, process dependent <br> $g_{d 0}$, size dependent |
|  | Flicker | $f$ |
| CS-stage | Thermal, gate resistance | $\frac{W}{L}$, N-fingers |
| CG-stage | Thermal+flicker | $\frac{\gamma}{g_{m} R_{s}}+1$ |
| Resistive load | Thermal | $1+\gamma+4 \frac{R_{S}}{R_{L}}$ |
| Tuned load | Thermal | $R$ |

* Parasitic series resistance in the LC-tank, layout dependent.

The specification for reverse isolation is, $S_{12} \geq 60 \mathrm{~dB}$, and thus we employ $2 \times$ CG-stages. The additional CG-stage it's also beneficial in other ways:

+ Increase in output impedance.
+ Turn off the LNA half-circuit, by turning off the $1^{\text {st }}$ CG-stage.
+ Use of higher supply voltage without having to worry about reliability. In addition, it increases the effectivness of the LDO, due to reduced dropout voltage, going from $1.8-1.5 \mathrm{~V}$ as opposed to, 1.81.2 V using the nominal voltage supply.
- One of the major costs of cascoding is the reduced voltage headroom, so for two additional CG stages the voltage headroom (relative to the input stage) is reduced by $\approx 2 \cdot V_{t h}$, which might cause issue with regards to linearity and/or compression.

The extra CG-stages has similar dimensions as the input stage, albeit an increase in length to increase the gain.
In terms of biasing, we bias the $1^{\text {st }}$ CG-stage to the nominal supply of 1.2 V , and the $2^{\text {nd }}$ CG-stage is over-volted to the system supply voltage, 1.5 V. The $2^{\text {nd }}$ CG-stage is biased to the nominal supply to ensure that the $V_{G S}$ of the device does not surpass the nominal supply. This is done with regards to reliability.
One parameter that is closely related to reverse isolation is the stability of the LNA. We ensure that the amplifier is inherently stable regardless of the input/output impedances.
In terms of stability of a two-port network, it's common to use either the:

1. $\mathrm{K}-\Delta$ stability test
2. $\mu$-stability test,
where:

$$
\begin{align*}
& \mathrm{K}-\Delta \text { stability test: } K>1 \&|\Delta|<1 \\
& \quad \mu \text {-stability test: } \mu>1 \tag{3.24}
\end{align*}
$$

with

$$
\begin{gather*}
\Delta=S_{11} S_{22}-S_{12} S_{21}  \tag{3.25}\\
K=\frac{1-\left|S_{11}\right|^{2}-\left|S_{22}\right|^{2}+|\Delta|^{2}}{2\left|S_{12}\right|\left|S_{21}\right|}  \tag{3.26}\\
\mu=\frac{1-\left|S_{11}\right|^{2}}{\left|S_{11}^{*} \Delta-S_{22}\right|+\left|S_{12} S_{21}\right|} \tag{3.27}
\end{gather*}
$$

From Eqs. (3.26) and (3.27), we see that reverse isolation, $\left|S_{12}\right|$ impacts the stability of the amplifier, so we maximize the reverse isolation.
We want an unconditionally stable LNA, thus, $\mu \geq 1$.
A figure of merit in terms of linearity is the -1 dB compression point ( P 1 dB ), which tells the input power at which the gain $\left(A_{V}\right)$ compresses by 1 dB . The P1dB can be expressed as [15]:

$$
\begin{equation*}
P_{i n,-1 \mathrm{~dB}}=\sqrt{0.145\left|\frac{\alpha_{1}}{\alpha_{3}}\right|}, \tag{3.28}
\end{equation*}
$$

where $\alpha_{1}$ is the DC quantity arising from second-order nonlinearity, and $\alpha_{2}$ is the fundamental harmonic.
We want to have a P 1 dB specification of $>-30 \mathrm{dBm}$
The biasing for the input stage $\left(M_{1}\right)$ is designed around the ' $g_{m} / I_{d}$ design methodology' [17].
From specifications, we have a fixed current of 2.5 mA per half-circuit, and $g_{m} \& V_{O V}$ can then be derived. We know that the $g_{m}$ of the device affects the gain, noise figure and the input impedance. Maximizing $g_{m}$ is done through maximizing the efficiency, $g_{m} / I_{d}$, which in turn reduces $V_{O V}$. By reducing $V_{O V}$, one increases the dimension of the active devices, for a fixed current. For a fixed amount of current, we have the following trades-offs:

- Low Vov (weak inversion):
+ High efficiency, $g_{m} / I_{d}$
- Low current density, $I_{d} / W \rightarrow$ larger devices
- Biased at the edge of saturation region $\rightarrow$ lower linearity
- High Vov (strong inversion):
+ Higher current density, $I d / W \rightarrow$ smaller devices
+ Deeply biased in saturation region $\rightarrow$ higher linearity
- Lower efficiency, $g_{m} / I_{d}$

One biases between two extremes, i.e. at medium inversion. This gives us a compromise between efficiency, gain and active area. Choosing an efficiency of $g_{m} / I_{d}=10 \mathrm{~V}^{-1}, V_{O V}$ is [17].

$$
\begin{equation*}
V_{O V}=\frac{2 I_{d}}{g_{m}}=\frac{2}{g_{m} / I_{d}}=\frac{2}{10 \mathrm{~V}^{-1}}=200 \mathrm{mV} \tag{3.29}
\end{equation*}
$$

From $g_{m}$ and $I_{d}$, we have

$$
\begin{equation*}
V_{O V}=\frac{2 I_{d}}{g_{m}}=\frac{2 \cdot 2.5 \mathrm{~mA}}{30 \mathrm{mS}} \approx 167 \mathrm{mV} \tag{3.30}
\end{equation*}
$$

As a rule of thumb, $V_{O V}=0.1 \cdot V_{D D}=150 \mathrm{mV}$.
Moving forward, the overdrive voltge is set to $V_{O V}=170 \mathrm{mV}$. The input stage is implemented using low-voltage threshold (LVT) devices from TSMC, the gate-source voltage, $V_{G S}$ is:

$$
\begin{equation*}
V_{G S}=V_{O V}+V_{t h}=170 \mathrm{mV}+380 \mathrm{mV}=550 \mathrm{mV} \tag{3.31}
\end{equation*}
$$

A resistive divider (see Fig. 3.1) is used for biasing the input stage.


Figure 3.2: Biasing parameters used for the ' $g_{m} / I_{d}$ design methodology'. Simulated on an nMOS device with $W / L=1 \mu \mathrm{~m} / 60 \mathrm{~nm}$.

Table 3.9: Biasing parameters derived using the $g m / I d$ method

| Parameter | Realization | Design specification |
| :---: | :---: | :---: |
| $V_{\text {gate }}$ | 447 mV | 550 mV |
| $V_{\text {source }}$ | 8.337 mV | 0 V |
| $V_{t h}$ | 382.6 mV | 380 mV |
| $V_{O V}$ | 54.34 mV | 170 mV |
| $g_{m} / I_{d}$ | $12.67 \mathrm{~V}^{-1}$ | $10 \mathrm{~V}^{-1}$ |
| $g_{m}$ | 36.31 mS | 30 mS |
| $I_{d}$ | 2.865 mA | 2.5 mA |

The LNA core is build using the PCells from TSMC's 65 nm library, where deep $N$-well 6 T or six terminal low threshold voltage RF active devices are chosen because of their well modelled parasitic capacitance and noise isolation properties.
To reduce gate resistance, we realize MOSFETs with the maximum fingers. The gate noise as a function of fingers, can be modelled as [12]:

$$
\begin{equation*}
\overline{V_{n}^{2}}=4 k T \frac{R_{G}}{3} \frac{1}{N^{2}}, \tag{3.32}
\end{equation*}
$$

where k is the Boltzmann's constant, T is the temperature in kelvin, $R_{G}$ is the gate resistance, as a function of the polysilicon sheet resistance, and N is the amount of fingers for the device. Increasing the fingers reduces the resistance at the gate, as well as at the drain of the MOSFET.
The LC-tank is realized using TSMC's scalable center-tapped inductor model (spiral_sym_ct_std_mu_z), and the capacitor is realized using TSMC's scalable MoM capacitors (crtmom_rf)
The center-tapped inductor offers common-mode rejection, higher selfinductance and higher Q when driven differentially [11].
The resistive divider is built with unit-cells, to minimize mismatch. The resistors are implemented in a $4 \times \mathrm{N}$ manner, where the rest of the array is filled up with dummy devices. $R_{1}$ is realized using a total of 8 unit cells, where 7 of them are active, and 1 is a dummy. $R_{2}$ is realized using a total 4 units, where 3 of them are active, and 1 is a dummy. Thus, $V_{G S}$ is:

$$
\begin{equation*}
V_{G S}=\frac{R_{2}}{R_{1}+R_{2}} V_{D D}=\frac{3}{10} 1.5 \mathrm{~V}=450 \mathrm{mV} \tag{3.33}
\end{equation*}
$$



Figure 3.3: Layout of the LNA

Table 3.10: Design specifications of the LNA

| Gain | $\geq 30 \mathrm{~dB}$ |
| :---: | :---: |
| Bandwidth $^{*}$ | $\geq 1 \mathrm{GHz}$ |
| $S_{11}$ | $\leq-10 \mathrm{~dB}$ |
| $S_{12}$ | $\geq 60 \mathrm{~dB}$ |
| NF | $\leq 3 \mathrm{~dB}$ |
| P1dB | $\geq-30 \mathrm{dBm}$ |
| DC current ${ }^{\dagger}$ | $\leq 2.5 \mathrm{~mA}$ |
| Power supply | 1.5 V |

### 3.3 Simulation results

The return loss $\left(S_{11}\right)$ of the LNA, shows how well the LNA is matched to a differential source impedance of $100 \Omega$. The maximum return loss is at the upper frequency ( 8.375 GHz ) of the passband, and is $S_{11 \mid \text { max }}=$ -10.45 dB . The bandwidth for $S_{11} \leq-10 \mathrm{~dB}$ is $5.6-8.6 \mathrm{GHz}$.
From Fig. 3.5a, the real impedance, is off by $\approx 40 \Omega$ in the band of interest. The reason for this is the change in the biasing condition of the input stage (ground bounce) ${ }^{2}$ this causes the $V_{O V}$ to be lower, and $g_{m}$ to be higher $\left(\frac{2 I_{d}}{g_{m}}=V_{O V}\right)$ for a constant current.
The output return loss ( $S_{22}$ ) of the LNA, shows how well the LNA is matched to a differential $100 \Omega$ load. The maximum return loss is at the upper frequency ( 8.375 GHz ) of passband, and is: $S_{22 \mid \max }=-13.33 \mathrm{~dB}$. The bandwidth for $S_{22} \leq-10 \mathrm{~dB}$ is from $1-12 \mathrm{GHz}$. For the desired $S_{22} \leq-20 \mathrm{~dB}$, are out of band ( $<4.6 \mathrm{GHz}$ ).
For the voltage gain \& bandwidth, we look at the transfer function for the LNA. The voltage gain is reported as the maximum gain, while the bandwidth is reported as the -10 dB -bandwidth.
In terms of maximum gain, we have:

$$
\begin{align*}
A_{V \mid \max } & =20.1 \mathrm{~dB}, f=8.1 \mathrm{GHz}  \tag{3.34}\\
A_{V \mid \text { no-buffer }, \max } & =27.3 \mathrm{~dB}, f=8.1 \mathrm{GHz}
\end{align*}
$$

From the maximum gain, we can see that the buffers show a loss of 7.2 dB $(27.3 \mathrm{~dB}-20.1 \mathrm{~dB})$. We see that the resonant frequency of the LC-tank is 8.1 GHz .

[^2]

Figure 3.4: $S_{11}$ of the LNA.


Figure 3.5: Real \& imaginary input impedances of the LNA.

The -10 dB BW is:

$$
\begin{equation*}
B W=8.8 \mathrm{GHz}-7.3 \mathrm{GHz}=1.5 \mathrm{GHz}, \tag{3.35}
\end{equation*}
$$

relative to the center frequency, the BW is:

$$
\begin{equation*}
B W=f_{c} \pm \frac{B W}{2} \approx 8.1 \mathrm{GHz} \pm 750 \mathrm{MHz} \tag{3.36}
\end{equation*}
$$

Compared to the specifications, we see that the gain is off by $3 \mathrm{~dB}^{3}$, the center frequency is off by 300 MHz . The bandwidth meets the 1 GHz requirement at -10 dB .

[^3]

Figure 3.6: $S_{22}$ of the LNA.


Figure 3.7: Real \& imaginary output impedance of the LNA.

For the reverse isolation, we look at the transfer function from the output to the input, i.e. the reverse gain of the LNA. The minimum reverse isolation is found at the upper frequency $(8.375 \mathrm{GHz})$ of the passband, and is $S_{12 \mid \text { min }}=43.18 \mathrm{~dB}$
The reverse isolation is $\approx 17 \mathrm{~dB}$ worse than specified. What matters is that the LNA is unconditionally stable.
To measure stability we will use the $\mu$-stability test, which tells us whether the LNA is unconditionally stable ( $\mu \geq 1$ ). The minimum value of $\mu$ is found in the upper frequency ( 8.2 GHz ) of the passband, and is $\mu_{\text {min }}=3.66$.
As shown in Fig. 3.10, we meet the condition for unconditionally stability from $6-10 \mathrm{GHz}$.


Figure 3.8: Frequency response of the LNA using S-parameter $S_{21}$


Figure 3.9: Reverse isolation of the LNA

The noise figure (NF) in the passband is


Figure 3.10: $\mu$-factor for stability

|  | $f_{c}-500 \mathrm{MHz}$ | $f_{c}$ | $f_{c}+500 \mathrm{MHz}$ |
| :---: | :---: | :---: | :---: |
| $N F$ | 4.28 dB | 4.22 dB | 4.38 dB |
| $N F_{\text {min }}$ | 4.27 dB | 4.20 dB | 4.29 dB |

Compared to the specifications, we are 1 dB off. Nonetheless, we are noise matched.
For 1 dB compression point (P1dB), we use large signal S-parameters (LSSP), were we sweep the input power, $P_{R F}$
Input and output return loss is $\ll-10 \mathrm{~dB}$ for $P_{R F}>0 \mathrm{dBm}$ (see
Figs. 3.12a and 3.12c). From Fig. 3.12b, the P1dB is -17 dBm , which is 13 dBm higher than required.
The total current consumption, $I_{D C}$, of the LNA is $10 \mathrm{~mA} . I_{D C}$ is the sum of the current per branch $(2 \times 3 \mathrm{~mA})$ and per buffer $(2 \times 2 \mathrm{~mA})$. The total power, $P_{D C}$, is 15 mW from a 1.5 V supply.

### 3.4 Conclusion

In this chapter we've looked at the background theory, the specifications required, as well as realized an LNA for the UWB frequency spectrum. This realization will be used as a baseline for the implementation of a RFreceiver front-end moving onward.


Figure 3.11: Noise figure for the LNA


Figure 3.12: LSSP at $f_{c}=7.875 \mathrm{GHz}$ with $P_{R F}$ from -30 dBm to 10 dBm

Table 3.11: Summary of simulation results.

| Gain | 27.3 dB* |
| :---: | :---: |
| Bandwidth ${ }^{\dagger}$ | 1.5 GHz |
| $\begin{gathered} S_{11} \\ \text { Minimum } \\ \text { Desired }^{\S} \end{gathered}$ | $\begin{gathered} 5.6-8.6 \mathrm{GHz} \\ \mathrm{~N} / \mathrm{A} \end{gathered}$ |
| $\begin{gathered} S_{22} \\ \text { Minimum\\| } \\ \text { Desired } \end{gathered}$ | $\begin{gathered} \text { 1-12 GHz } \\ \text { N/A } \end{gathered}$ |
| RI | $\geq 43.18 \mathrm{~dB}$ |
| Stability, $\mu$-factor | > 3.83 |
| NF | $\leq 4.38 \mathrm{~dB}$ |
| P1dB | $-17 \mathrm{dBm}$ |
| DC current | $6 \mathrm{~mA}^{*}$ |

[^4]
## Chapter 4

## Area Optimized and Passively Reconfigurable RF Front-Ends

RF receivers have to condition and amplify the received signal to a sufficient level. This can happen in multiple stages. The receiver chain core comprises filter (for signal conditioning), low-noise-amplifier (for frequency selectivity and amplification) and ADC (digitisation). Unlike direct-RF, low-/zero-IF architectures require additional mixing and filtering stages.
Thus, direct-RF has benefits, such as relaxed filtering, no LO-leakage, no quadrature LO, and in general a simpler receiver chain [18]. However, the ADC noise contribution is higher, due to lack of additional gain from the mixing and baseband stage(s) [19].


Figure 4.1: Comparison between heterodyne and direct RF-sampling receivers [19]

We will build a front-end, which consists of the HPF is Chapter 2 and an LNA in Chapter 3.
In this chapter, we propose two different RFFEs. The first one being an area optimized RFFE for single-antenna use. The second a passively reconfigurable RFFE.

### 4.1 Area-optimized RFFE



Figure 4.2: SEDE-HPF


Figure 4.3: Reactive feedback/feedforward LNA


Figure 4.4: Area-optimized RFFE, block level
The area-optimized RFFE comprises of the single-ended to differential (SEDE) HPF from Chapter 2, and the LNA from Chapter 3. No additional modifications were done.


Figure 4.5: Layout of the area-optimized RFFE

### 4.1.1 Simulation results

The maximum return loss ( $S_{11}$ ) of the RFFE, for a single-ended source impedance of $50 \Omega$ is measured at 8.375 GHz and is -12.54 dB . The $S_{11}$ -10 dB -bandwidth is $6.5-9.7 \mathrm{GHz}$. The $S_{11}-20 \mathrm{~dB}$-bandwidth is 7.17.6 GHz .


Figure 4.6: $S_{11}$ of the area-optimized RFFE


Figure 4.7: Real \& imaginary input impedance of the area-optimized RFFE

The maximum output return loss ( $S_{22}$ ) of the RFFE, for a differential load impedance of $100 \Omega$ is measured at 8.375 GHz and is -13.37 dB . The $S_{22}-10 \mathrm{~dB}$-bandwidth is $1-10 \mathrm{GHz}$. The -20 dB -bandwidth is out-of-band ( $\leq 4.4 \mathrm{GHz}$ ).


Figure 4.8: $S_{22}$ of the area-optimized RFFE


Figure 4.9: Real \& imaginary output impedance of the area-optimized RFFE

The voltage gain $\left(A_{V}\right)$ is reported as the maximum gain, and the bandwidth is reported as the -10 dB -bandwidth. The maximum gain at the resonant frequency 8.1 GHz is 17.39 dB . The -10 dB BW is 1.4 GHz .
Relative to the resonant frequency the BW is $\pm 700 \mathrm{MHz}$.
Attenuation at the bands of interest are:

$$
\text { Rejection }=\left\{\begin{array}{l}
\geq 37.45 \mathrm{~dB}, \quad f \in[0,2.4] \mathrm{GHz}  \tag{4.1}\\
\geq 10.97 \mathrm{~dB}, \quad f \in[2.4,5.0] \mathrm{GHz} \\
10.54 \mathrm{~dB}, \quad f=5.8 \mathrm{GHz}
\end{array}\right.
$$



Figure 4.10: Voltage gain of the area-optimized RFFE

The minimum reverse isolation is -47.42 at 7.375 GHz (passband).


Figure 4.11: Reverse isolation of the area-optimized RFFE
For stability, we use the $\mu$-stability test, if $\mu>1$, the RFFE is unconditionally stable. The minimum value of $\mu$ is $\mu_{\text {min }}=4.18$ at 8.375 GHz (passband).
As shown in Fig. 5.27, we meet the condition for unconditionally stability from $6-10 \mathrm{GHz}$.


Figure 4.12: $\mu$-factor for stability

The noise figure (NF) in the passband is

|  | $f_{c}-500 \mathrm{MHz}$ | $f_{c}$ | $f_{c}+500 \mathrm{MHz}$ |
| :---: | :---: | :---: | :---: |
| NF | 7.48 dB | 6.99 dB | 7.02 dB |
| $\mathrm{NF}_{\text {min }}$ | 7.47 dB | 6.95 dB | 6.92 dB |



Figure 4.13: Noise figure for the area-optimized RFFE

For the 1 dB compression point (P1dB), we use large signal S-parameters (LSSP), where we sweep the input power, $P_{R F}$.From Fig. 5.28, the P1dB is -17 dBm , which 13 dB higher than required.


Figure 4.14: Large signal S-parameter, $S_{21}$, for area-optimized RFFE
The total current consumption, $I_{D C}$ of the area-optimized RFFE in is 10 mA . The active core consumes 5.6 mA and the buffers 4.4 mA .

|  | Results | Specification |
| :---: | :---: | :---: |
| Gain ${ }^{\ddagger}$ | 24.57 dB | 30 dB |
| Bandwidth* | $7.4-8.8$ GHz | $7.375-8.375 \mathrm{GHz}$ |
| Rejection: |  |  |
| @ 2.4 GHz | 37.45 dB | 20 dB |
| @ 5 GHz | 10.97 dB | 15 dB |
| @ 5.8 GHz | 10.54 dB | 15 dB |
| $S_{11}$ : |  |  |
| Minimum ${ }^{\dagger}$ | $6.5-9.7 \mathrm{GHz}$ | $7.375-8.375 \mathrm{GHz}$ |
| Desired ${ }^{\S}$ | $7.1-7.6 \mathrm{GHz}$ | $7.375-8.375 \mathrm{GHz}$ |
| $S_{22}$ : |  |  |
| Minimum ${ }^{\dagger}$ | $1-10 \mathrm{GHz}$ | $7.375-8.375 \mathrm{GHz}$ |
| Desired ${ }^{\S}$ | $\leq 4.4 \mathrm{GHz}$ | $7.375-8.375 \mathrm{GHz}$ |
| Reverse isolation | $\geq 47.42 \mathrm{~dB}$ | $\geq 60 \mathrm{~dB}$ |
| Stability, $\mu$-factor | $\geq 4.18$ | $\geq 1$ |
| NF | $\leq 7.48 \mathrm{~dB}$ | $\leq 7 \mathrm{~dB}$ |
| P1dB | $-22 \mathrm{dBm}$ | $-30 \mathrm{dBm}$ |
| DC current** | 5.6 mA | 6 mA |
| Area | $1.1 \mathrm{x} 0.86 \mathrm{~mm}^{2}$ |  |
| Supply voltage | 1.5 V |  |
| * -10 dB |  |  |
| $\dagger \leq-10 \mathrm{~dB}$ |  |  |
| § $\leq-20 \mathrm{~dB}$ |  |  |
| ** Active core only |  |  |
| $\ddagger$ De-embedded bu |  |  |

### 4.2 Passively reconfigurable RFFE

From literature, we know that full-duplex doubles the spectral capacity compared to half-duplex. Off-chip RF transceivers generally use bulky ferrite circulators to achieve the required TX-RX isolation. For CMOS integration, we rely on non-magnetic circulators with small form factors. In [6], the authors present an active 2-port full-duplex bi-directional frequency converter (BDFC).
The BDFC achieves +25 dB TX-ANT/ANT-RX isolation, with an insertion loss of 3 dB , at a staggering 48 mW power consumption.


Figure 4.15: Full schematic of the BDFC-based front-end [6]
In contrast to this work, we propose a passively reconfigurable RFFE. For TX-RX isolation, we cancel out the 'common-mode' TX-signal at the output of the LNA by transformer-action. By applying the TX as a common-mode signal across the LC-tank in the LNA, we achieve the required TX-RX cancelllation for full-duplex operation without adding to the power budget.
To transform the TX as a common mode signal, we modify the LNA in Chapter 3. This is shown in Fig. 4.17. A third duplicate (AUX) branch of either of the input-branches is required. One input (say negative CSstage) is shunted to ground through a switch. The $1^{\text {st }}$ CG-stage is opened (i.e., gate is pulled low). The AUX amplifies a copy of the transmitted signal via the positive branch (TRX) and connects to the drain of the $2^{\text {nd }}$ CG-stage. This gives us the single-ended to pseudo-differential configuration. To ensure that the copy of the transmitted signal is equal
to the 'real' transmitted signal, we ensure that the impedances of the two branches are equal. Thus, assuming perfect match between the TRX branch, the AUX branch, we thus expect the signal at the output of the LNA to be:

$$
\begin{align*}
V_{T R X} & =V_{R X \mid T R X}+V_{T X \mid T R X} \\
V_{A U X} & =V_{T X \mid A U X} \\
V_{o u t} & =A_{V}\left(V_{T R X}+V_{A U X}\right)  \tag{4.2}\\
& =V_{R X} \cdot A_{D}+\left(V_{T X \mid T R X}-V_{T X \mid A U X}\right) A_{C M} \\
& \approx V_{R X \mid T R X},
\end{align*}
$$

where $A_{D}$ is the differential gain of the LNA, $A_{C M}$ is the common-mode rejection of the LC-tank. Assuming sufficient common-mode rejection, we will see the single-ended RX signal gained by the differential gain at the output.
We require at least -20 dB TX-RX cancellation, i.e. a common-mode rejection of 20 dB . In terms of insertion loss, we have an intrinsic loss of 6 dB in TX/RX due to the single-ended configuration.
The passively reconfigurable RFFE (PR-RFFE) can operate in halfduplex (HD) and full-duplex (FD) mode. In it's natural HD-mode, it is configured as pseudo-differential to pseudo-differential. In FD-mode, it is configured as single-ended to pseudo-differential. The PR-RFFE comprises of the pseudo-differential to pseudo-differential (PDPD) HPF from Chapter 2, and a modified LNA from Chapter 3. In addition, we need to supply a TX-like signal to test the FD-mode, hence a TX-driver was made in addition. This TX-driver was input impedance matched using a $50 \Omega$ resistance. This TX-driver is not considered as part of the RFFE, but rather a test-structure. Unfortunately, we cannot de-embed the TX-driver.

To ensure amplitude \& phase matching between the TRX+-branch and the AUX branch, we have added an additional RC-matching network to the AUX. The RC-matching compensates for the impedance of the HPF seen single-endedly. Ideally, we expect the impedance looking into the interface between the LNA and the HPF to be $25 \Omega(50 \Omega \| 50 \Omega)$. The added resistance is $40 \Omega$ and the added capacitance is 1.4 pF .


Figure 4.16: PR-RFFE filter, PDPD-HPF from Chapter 2


Figure 4.17: PR-RFFE LNA branch for full-duplex mode


Figure 4.18: PR-RFFE TX-driver stage


Figure 4.19: PR-RFFE, block level

From Fig. 4.19, we see the total system level for the PR-RFFE. The TXdriver consists of $2 \times$ TX-driver stages from Fig. 4.18. The LNA consists of $3 \times$ LNA-FD slices from Fig. 4.17 in addition to the summing stage, which is the LC-tank from the LNA in Chapter 3.


Figure 4.20: Layout PR-RFFE

### 4.3 Simulation results

The simulation results were done by taking the EMX model of the HPF and the EMX model of the LNA, and then connecting these two together, and we get the following results

### 4.3.1 Half-duplex mode

The maximum return loss $\left(S_{11}\right)$ of the RFFE, for a differential source impedance of $100 \Omega$ is measured at 8.375 GHz and is -15.48 dB . The $S_{11}-10 \mathrm{~dB}$-bandwidth is $6.1-8.9 \mathrm{GHz}$. The $S_{11}-20 \mathrm{~dB}$-bandwidth is at a single point 8.1 GHz .


Figure 4.21: $S_{11}$ of the PR-RFFE in HD-mode


Figure 4.22: Real \& imaginary input impedances of the PR-RFFE in HDmode

The maximum output return loss $\left(S_{22}\right)$ of the RFFE, for a differential load impedance of $100 \Omega$ is measured at 7.375 GHz and is -10.58 dB . The $S_{22}-10 \mathrm{~dB}$-bandwidth is $7.4-10 \mathrm{GHz}$.


Figure 4.23: $S_{22}$ of the PR-RFFE in HD-mode


Figure 4.24: Real \& imaginary output impedances of the PR-RFFE in HD-mode

The voltage gain $\left(A_{V}\right)$ is reported as the maximum gain, and the bandwidth is reported as the -10 dB -bandwidth. The maximum gain at the resonant frequency 7.4 GHz is 26.3 dB . The -10 dB BW is 1.3 GHz . Relative to the resonant frequency the BW is $\pm 650 \mathrm{MHz}$.
Attenuation at the bands of interest are:

$$
\text { Rejection }=\left\{\begin{array}{l}
\geq 27.33 \mathrm{~dB}, f \in[0,2.4] \mathrm{GHz},  \tag{4.3}\\
\geq 18.71 \mathrm{~dB}, f \in[2.4,5.0] \mathrm{GHz}, \\
-6.11 \mathrm{~dB}, f=5.8 \mathrm{GHz}
\end{array}\right.
$$

The aforementioned meets specifications.


Figure 4.25: Voltage gain of the PR-RFFE in HD-mode

The minimum reverse isolation is at 7.375 GHz , and is -70.07 dB .


Figure 4.26: Reverse isolation of the PR-RFFE in HD-mode
For stability, we use the $\mu$-stability test, if $\mu>1$, the RFFE is unconditionally stable. The minimum value of $\mu$ is $\mu_{\text {min }}=3.95$ at 7.375 GHz (passband).
As shown in Fig. 5.8, we meet the condition for unconditionally stability from $6-10 \mathrm{GHz}$.


Figure 4.27: $\mu$-factor

The noise figure (NF) in the passband is:

|  | $f_{c}-500 \mathrm{MHz}$ | $f_{c}$ | $f_{c}+500 \mathrm{MHz}$ |
| :---: | :---: | :---: | :---: |
| NF | 5.68 dB | 5.41 dB | 5.54 dB |
| $\mathrm{NF}_{\text {min }}$ | 5.68 dB | 5.37 dB | 5.41 dB |



Figure 4.28: Noise figure for the PR-RFFE in HD-mode

For the 1 dB compression point (P1dB), we use large signal S-parameters (LSSP), where we sweep the input power, $P_{R F}$. From Fig. 5.9, the P1dB is -16 dBm , which is 14 dB higher than required.


Figure 4.29: Large signal S-parameter, $S_{21}$, for PR-RFFE in HD-mode The total current consumption, $I_{D C}$ of the PR-RFFE in HD-mode is 27 mA . The active core consumes 7 mA , and the buffers 20 mA .

### 4.3.2 Full-duplex mode

The maximum return loss ( $S_{11}$ ) of the RFFE, for a differential source impedance of $50 \Omega$ is measured at 7.375 GHz and is -17.86 dB . The $S_{11}$ -10 dB -bandwidth is $6.2-9.2 \mathrm{GHz}$. The $S_{11}-20 \mathrm{~dB}$-bandwidth is $7.6-$ 8.2 GHz .


Figure 4.30: $S_{11}$ of the PR-RFFE in FD-mode


Figure 4.31: Real \& imaginary input impedance of the PR-RFFE in FDmode

The maximum output return loss ( $S_{22}$ ) of the RFFE, for a differential load impedance of $100 \Omega$ is measured at 7.375 GHz and is -10.61 dB . The $S_{22}-10 \mathrm{~dB}$-bandwidth is $7.4-10 \mathrm{GHz}$.


Figure 4.32: $S_{22}$ of the PR-RFFE in FD-mode


Figure 4.33: Real \& imaginary output impedance of the PR-RFFE in FDmode

The maximum input return loss for $\mathrm{TX}\left(S_{33}\right)$ of the RFFE, for a differential source impedance of $100 \Omega$ is measured at 8.375 GHz and is -11.56 dB . The $S_{33}-10 \mathrm{~dB}$-bandwidth is $1-10 \mathrm{GHz}$.


Figure 4.34: $S_{33}$ of the PR-RFFE in FD-mode


Figure 4.35: Real \& imaginary input impedances on TX port of the PRRFFE in FD-mode

The voltage gain $\left(A_{V}\right)$ is reported as the maximum gain, and the bandwidth is reported as the -10 dB -bandwidth. The maximum gain at the resonant frequency 7.4 GHz is 23.2 dB . The -10 dB BW is 1.3 GHz . Relative to the resonant frequency the BW is $\pm 650 \mathrm{MHz}$.
Attenuation at the bands of interest are:

$$
\text { Rejection }=\left\{\begin{array}{l}
\leq 30.15 \mathrm{~dB}, f \in[0,2.4] \mathrm{GHz}  \tag{4.4}\\
\leq 21.87 \mathrm{~dB}, \quad f \in[2.4,5.0] \mathrm{GHz} \\
10 \mathrm{~dB}, f=5.8 \mathrm{GHz}
\end{array}\right.
$$



Figure 4.36: Voltage gain of the PR-RFFE in FD-mode

The voltage gain from TX to the RX port $\left(A_{V \mid T X-R X}\right)$ is reported as the maximum gain. The maximum gain is -9.44 dB at the resonance frequency of the LNA $(7.4 \mathrm{GHz})$. The maximum gain looking into the antenna from the TX is $A_{V \mid T X-A N T}=-15.1 \mathrm{~dB}$. Thus, the TX-RX isolation is:

$$
\begin{equation*}
A_{V \mid T X-R X}=\left(S_{23}+\left|S_{13}\right|\right)-S_{21} \tag{4.5}
\end{equation*}
$$

where $S_{23}$ is the unwanted forward transmission gain from TX to RX, $S_{13}$ is the interface loss from TX to RX, and $S_{21}$ is the voltage gain. From Figs. 5.16 and 5.17, we have:

$$
A_{V \mid T X-R X}=(-9.44 \mathrm{~dB}+|-15.09 \mathrm{~dB}|)-18.16 \mathrm{~dB}=-12.51 \mathrm{~dB}
$$ or $\approx 12.5 \mathrm{~dB}$ cancellation. Due to the high loss at the interface, it is difficult to de-embed whether this is the actual cancellation or not.


$\left.\begin{array}{|cc|}\hline- & \text { Max: }(7.4,-9.44) \\ \square & -10 \mathrm{~dB}:(6.8,-13.46) \\ \otimes & -10 \mathrm{~dB}:(8.8,7.80)\end{array}\right]$
(a) TX to RX

(b) TX to ANT

Figure 4.37: Forward transmission gain, using TX as input port

The minimum reverse isolation is -73.31 at 7.375 GHz (passband).


Figure 4.38: Reverse isolation of the area-optimized RFFE
For stability, we use the $\mu$-stability test, if $\mu>1$, the RFFE is unconditionally stable. The minimum value of $\mu$ is $\mu_{\text {min }}=3.38$ at 7.375 GHz (passband). As shown in Fig. 4.40, we meet the condition for unconditionally stability from $6-10 \mathrm{GHz}$.


Figure 4.39: $\mu$-factor for stability

The noise figure (NF) in the passband is:

|  | $f_{c}-500 \mathrm{MHz}$ | $f_{c}$ | $f_{c}+500 \mathrm{MHz}$ |
| :---: | :---: | :---: | :---: |
| NF | 9.76 dB | 9.46 dB | 9.5 dB |
| $\mathrm{NF}_{\text {min }}$ | 9.75 dB | 9.45 dB | 9.40 dB |



Figure 4.40: Noise figure for the PR-RFFE in FD-mode

For 1 dB compression point (P1dB), we use large signal S-parameters (LSSP), where we sweep the input power, $P_{R F}$. From Fig. 5.20, the P1dB is -10.8 dBm , which is 20 dB higher than required.


Figure 4.41: Large signal S-parameter, $S_{21}$, PR-RFFE in FD-mode
The total current consumption, $I_{D C}$ of the PR-RFFE in FD-mode is 37.4 mA . The active core consumes 7.3 mA , the buffers 20 mA and the TXdrivers 10 mA .

|  | Results |  | Specification |
| :---: | :---: | :---: | :---: |
|  | HD | FD |  |
| Gain ${ }^{\ddagger}$ | 26.8 dB | 13.7 dB | $\geq 30 \mathrm{~dB}$ |
| Bandwidth* | $6.7-7.9 \mathrm{GHz}$ | $6.6-8.0 \mathrm{GHz}$ | $7.375-8.375 \mathrm{GHz}$ |
| Rejection: <br> @ 2.4 GHz <br> @ 5 GHz <br> @ 5.8 GHz | $\begin{gathered} 27.33 \mathrm{~dB} \\ 18.71 \mathrm{~dB} \\ 6.1 \mathrm{~dB} \end{gathered}$ | $\begin{gathered} 30.15 \mathrm{~dB} \\ 21.87 \mathrm{~dB} \\ 9.67 \mathrm{~dB} \end{gathered}$ | $\begin{aligned} & 20 \mathrm{~dB} \\ & 20 \mathrm{~dB} \\ & 15 \mathrm{~dB} \end{aligned}$ |
| $\begin{gathered} S_{11}: \\ \text { Minimum }^{\dagger} \\ \text { Desired }^{\S} \end{gathered}$ | $\begin{gathered} 6.1-8.9 \mathrm{GHz} \\ 8.1 \mathrm{GHz} \end{gathered}$ | $\begin{aligned} & 6.2-9.2 \mathrm{GHz} \\ & 7.6-8.2 \mathrm{GHz} \end{aligned}$ | $\begin{aligned} & 7.375-8.375 \mathrm{GHz} \\ & 7.375-8.375 \mathrm{GHz} \end{aligned}$ |
| $\begin{gathered} S_{22}: \\ \text { Minimum }^{\dagger} \\ \text { Desired }^{\S} \end{gathered}$ | $\begin{gathered} 7.4-10 \mathrm{GHz} \\ \mathrm{~N} / \mathrm{A} \end{gathered}$ | $\begin{gathered} 7.4-10 \mathrm{GHz} \\ \mathrm{~N} / \mathrm{A} \end{gathered}$ | $\begin{aligned} & 7.375-8.375 \mathrm{GHz} \\ & 7.375-8.375 \mathrm{GHz} \end{aligned}$ |
| $\begin{gathered} S_{33}: \\ \text { Minimum }^{\dagger} \\ \text { Desired }^{\S} \end{gathered}$ | $\begin{aligned} & \mathrm{N} / \mathrm{A} \\ & \mathrm{~N} / \mathrm{A} \end{aligned}$ | $\begin{aligned} & 1-10 \mathrm{GHz} \\ & \leq 2.6 \mathrm{GHz} \end{aligned}$ | $\begin{aligned} & 7.375-8.375 \mathrm{GHz} \\ & 7.375-8.375 \mathrm{GHz} \end{aligned}$ |
| Reverse isolation | $\geq 70 \mathrm{~dB}$ | $\geq 50 \mathrm{~dB}$ | $\geq 60 \mathrm{~dB}$ |
| TX-RX isolation | N/A | 5 dB | 20 dB |
| Stability, $\mu$-factor | $\geq 3.35$ | $\geq 4.46$ | $\geq 1$ |
| NF | $\leq 5.68 \mathrm{~dB}$ | $\leq 9.76 \mathrm{~dB}$ | $\leq 6 \mathrm{~dB}$ |
| P1dB | $-19 \mathrm{dBm}$ | $-22.1 \mathrm{dBm}$ | $-30 \mathrm{dBm}$ |
| DC current** | 7 mA | 7.3 mA | 6 mA |
| Area | $1.92 \mathrm{x} 0.86 \mathrm{~mm}^{2}$ |  |  |
| Supply voltage | 1.5 V |  |  |
| $\begin{aligned} & \text { * }-10 \mathrm{~dB} \\ & \dagger \leq-10 \mathrm{~dB} \\ & \S \leq-20 \mathrm{~dB} \\ & { }^{* *} \text { Active core only } \\ & \ddagger \text { De-embedded bu } \end{aligned}$ |  |  |  |

### 4.3.3 Summary

The passively reconfigurable RFFE consumes no additional power, but has an intrinsic loss of 6 dB because of the single-ended operation. Mismatch (including impedance) between the TRX and AUX branches will deteriorate the figure-of-merit, i.e. TX-RX isolation. The area efficient RFFE is optimal for single-ended antenna use. It comprises a single harmonic trap, out-of-band rejection is traded-off.

|  | PR-RFFE |  | AO-RFFE |
| :---: | :---: | :---: | :---: |
|  | HD | FD |  |
| Gain $^{\ddagger}$ | 26.8 dB | 13.7 dB | 24.57 dB |
| Bandwidth $^{*}$ | $6.7-7.9 \mathrm{GHz}$ | $6.6-8.0 \mathrm{GHz}$ | $7.4-8.8 \mathrm{GHz}$ |
| Rejection: |  |  |  |
| @ 2.4 GHz | 27.33 dB | 30.15 dB | 37.45 dB |
| @ 5 GHz | 18.71 dB | 21.87 dB | 10.97 dB |
| @ 5.8 GHz | 6.1 dB | 9.67 dB | 10.54 dB |
| $S_{11}:$ |  |  |  |
| Minimum $^{\dagger}$ | $6.1-8.9 \mathrm{GHz}$ | $6.2-9.2 \mathrm{GHz}$ | $6.5-9.7 \mathrm{GHz}$ |
| Desired $^{\S}$ | 8.1 GHz | $7.6-8.2 \mathrm{GHz}$ | $7.1-7.6 \mathrm{GHz}$ |
| $S_{22}:$ |  |  |  |
| Minimum $^{\dagger}$ | $7.4-10 \mathrm{GHz}$ | $7.4-10 \mathrm{GHz}$ | $1-10 \mathrm{GHz}$ |
| Desired |  |  |  |

## Chapter 5

## Measurements

The RFFEs are fabricated in a $9(+1)$-metal layer 65 nm CMOS process. The microphotograph of the chip is shown in Fig. 5.1. Passive components, such as inductors, auto-transformers and transformers are implemented in the top ultra-thick-metal (UTM) layer (thickness > $3 \mu \mathrm{~m})$. The total die area is $4 \mathrm{~mm}^{2}\left(2 \mathrm{x} 2 \mathrm{~mm}^{2}\right)$ including the reconfigurable RFFE ( $1.92 \times 0.86 \mathrm{~mm}^{2}$ ) and the area efficient (1.1x0.86 $\mathrm{mm}^{2}$ ). Only for biasing, the IC is placed on a low frequency laminate with a 4.6 dielectric constant.
Our measurement setup comprised on-wafer test platform, vector network analyzer (VNA) and cables, up to $4 \times$ RF probes, and the device under test (DUT). For RF, on-wafer measurements and de-embedding can be challenging. All measurements are referenced to the probe-chip interface.


Figure 5.1: Microphotograph of the chip

### 5.1 Passively reconfigurable RFFE

For the passive reconfigurable RFFE (PR-RFFE), we use a total of $4 \times$ probes. In the half-duplex (HD) mode, we measure differentially, and thus, the two probe pairs have a $100 \Omega$ differential impedance. For fullduplex (FD) mode, we measure with $1 \times$ single-ended $50 \Omega$ probe (antenna port), $1 \times$ single-ended $50 \Omega$ probe (TX port) and $1 \times$ differential $100 \Omega$ probe ( RX port). For S-parameter, port mapping: Port $1=$ antenna $(\mathrm{ANT})$, Port $2=\mathrm{LNA}$ output $(\mathrm{RX})$, Port $3=$ transmitter input $(\mathrm{TX})$.

### 5.1.1 Half-duplex mode

The maximum return loss $\left(S_{11}\right)$ of the RFFE, for a differential source impedance of $100 \Omega$ is measured at 8.375 GHz and is -8.31 dB . The $S_{11}$ -10 dB -bandwidth is $6.2-8.1 \mathrm{GHz}$. The $S_{11}-20 \mathrm{~dB}$-bandwidth is $7.2-$ 7.6 GHz .


Figure 5.2: $S_{11}$ of the PR-RFFE in HD-mode


Figure 5.3: Real \& imaginary input impedances of the PR-RFFE in HDmode

The maximum output return loss $\left(S_{22}\right)$ of the RFFE, for a differential load impedance of $100 \Omega$ is measured at 8.375 GHz and is -11.98 dB . The $S_{22}-10 \mathrm{~dB}$-bandwidth is $1-10 \mathrm{GHz}$. The $S_{22}-20 \mathrm{~dB}$-bandwidth is 7-7.2 GHz.


Figure 5.4: $S_{22}$ of the PR-RFFE in HD-mode


Figure 5.5: Real \& imaginary output impedances of the PR-RFFE in HDmode
The voltage gain $\left(A_{V}\right)$ is reported as the maximum gain, and the bandwidth is reported as the -10 dB -bandwidth. In this RFFE, there was no de-embedding of output buffers. The maximum gain at the resonant frequency 7.2 GHz is 16.3 dB . The -10 dB BW is 1.2 GHz . Relative to the resonant frequency the BW is $\pm 600 \mathrm{MHz}$.
Attenuation at the bands of interest are:

$$
\text { Rejection }=\left\{\begin{array}{l}
\geq 31.88 \mathrm{~dB}, \quad f \in[0,2.4] \mathrm{GHz}  \tag{5.1}\\
\geq 20.90 \mathrm{~dB}, \quad f \in[2.4,5.0] \mathrm{GHz} \\
20.81 \mathrm{~dB}, \quad f=5.8 \mathrm{GHz}
\end{array}\right.
$$

The aforementioned meets specifications.


Figure 5.6: Forward voltage gain of the PR-RFFE in HD-mode

The minimum reverse isolation is at 7.875 GHz , and is -58.23 dB .

For values lower than -50 dB , we start approaching the noise floor of the measurement setup. Thus, all signals lower than -50 dB , will be reported as $<-50 \mathrm{~dB}$.


Figure 5.7: Reverse isolation of the PR-RFFE in HD-mode
For stability, we use the $\mu$-stability test, if $\mu>1$, the RFFE is unconditionally stable. The minimum value of $\mu$ is $\mu_{\text {min }}=3.95$ at 8.375 GHz (passband).
As shown in Fig. 5.8, we meet the condition for unconditionally stability from $6-10 \mathrm{GHz}$.


Figure 5.8: $\mu$-factor
For the 1 dB compression point (P1dB), we use large signal S-parameters (LSSP), where we sweep the input power, $P_{R F}$
From Fig. 5.9 , the P 1 dB is -19 dBm , which is 8 dB higher than required.


- P1dB: $(-19,14.70)$

Figure 5.9: Large signal S-parameter, $S_{21}$, for PR-RFFE in HD-mode The total current consumption, $I_{D C}$ of the PR-RFFE in HD-mode is 13.8 mA . The active core consumes 7.3 mA , and the buffers 6.5 mA . As a proof of concept, we will not pay too much attention to current consumption.

### 5.1.2 Full-duplex mode

The maximum return loss ( $S_{11}$ ) of the RFFE, for a differential source impedance of $50 \Omega$ is measured at 8.375 GHz and is -8.32 dB . The $S_{11}$ -10 dB -bandwidth is $6.2-8.1 \mathrm{GHz}$.


Figure 5.10: $S_{11}$ of the PR-RFFE in FD-mode


Figure 5.11: Real \& imaginary input impedance of the area-optimized RFFE.

The maximum output return loss $\left(S_{22}\right)$ of the RFFE, for a differential load impedance of $100 \Omega$ is measured at 8.375 GHz and is -13.36 dB . The $S_{22}-10 \mathrm{~dB}$-bandwidth is $1-10 \mathrm{GHz}$. The $S_{22}-20 \mathrm{~dB}$-bandwidth is a single point at 7.5 GHz .


Figure 5.12: $S_{22}$ of the PR-RFFE in FD-mode


Figure 5.13: Real \& imaginary output impedance of the PR-RFFE in FDmode

The maximum input return loss for TX $\left(S_{33}\right)$ of the RFFE, for a singleended source impedance of $50 \Omega$ is measured at 8.375 GHz and is -11.30 dB . The $S_{33}-10 \mathrm{~dB}$-bandwidth is $1-10 \mathrm{GHz}$. The $S_{33}-20 \mathrm{~dB}-$ bandwidth are out-of-bound ( $<2.6 \mathrm{GHz}$ ).


Figure 5.14: $S_{33}$ of the PR-RFFE in FD-mode


Figure 5.15: Real \& imaginary input impedances on TX port of the PRRFFE in FD-mode

The voltage gain $\left(A_{V}\right)$ is reported as the maximum gain, and the bandwidth is reported as the -10 dB -bandwidth. In this RFFE, there was no de-embedding of output buffers. The maximum gain at the resonant frequency 7.2 GHz is 13.7 dB . The -10 dB BW is 1.4 GHz . Relative to the resonant frequency the BW is $\pm 700 \mathrm{MHz}$.

Attenuation at the bands of interest are:

$$
\text { Rejection }=\left\{\begin{array}{l}
\leq 34.20 \mathrm{~dB}, \quad f \in[0,2.4] \mathrm{GHz}  \tag{5.2}\\
\leq 23.29 \mathrm{~dB}, \quad f \in[2.4,5.0] \mathrm{GHz} \\
23.51 \mathrm{~dB}, \quad f=5.8 \mathrm{GHz}
\end{array}\right.
$$



Figure 5.16: Forward voltage gain of the PR-RFFE in FD-mode The voltage gain from TX to the RX port $\left(A_{V \mid T X-R X}\right)$ is reported as the maximum gain. The maximum gain is -7.8 dB at the resonance frequency of the LNA $(7.2 \mathrm{GHz})$. The maximum gain looking into the antenna from the TX is $A_{V \mid T X-A N T}=-10.9 \mathrm{~dB}$. Thus, the TX-RX isolation is:

$$
\begin{equation*}
A_{V \mid T X-R X}=\left(S_{23}+\left|S_{13}\right|\right)-S_{21} \tag{5.3}
\end{equation*}
$$

where $S_{23}$ is the unwanted forward transmission gain from TX to RX, $S_{13}$ is the interface loss from TX to RX, and $S_{21}$ is the forward voltage gain.

From Figs. 5.16 and 5.17 , we have:

$$
A_{V \mid T X-R X}=(-7.8 \mathrm{~dB}+|-16.71 \mathrm{~dB}|)-13.73 \mathrm{~dB}=-4.82 \mathrm{~dB}
$$

or $\approx 5 \mathrm{~dB}$ cancellation. Due to the high loss at the interface, it is difficult to de-embed whether this is the actual cancellation or not.

(a) TX to RX

(b) TX to ANT

Figure 5.17: Forward transmission gain, using TX as input port The minimum reverse isolation is -65.10 at 8.375 GHz (passband). For values lower than -50 dB , we start approaching the noise floor of the measurement setup. Thus, all signals lower than -50 dB , will be reported


Figure 5.18: Reverse isolation of the area-optimized RFFE
For stability, we use the $\mu$-stability test, if $\mu>1$, the RFFE is unconditionally stable. The minimum value of $\mu$ is $\mu_{\text {min }}=4.46$ at 8.375 GHz (passband).
As shown in Fig. 4.40, we meet the condition for unconditionally stability from $6-10 \mathrm{GHz}$.


Figure 5.19: $\mu$-factor for stability

For 1 dB compression point (P1dB), we use large signal S-parameters (LSSP), where we sweep the input power, $P_{R F}$.From Fig. 5.20, the P1dB is -22 dBm , which is 8 dB higher than required.


Figure 5.20: Large signal S-parameter, $S_{21}$, PR-RFFE in FD-mode

The total current consumption, $I_{D C}$ of the PR-RFFE in FD-mode is 23.7 mA . The active core consumes 7.3 mA , the buffers 6.5 mA and the TX-drivers 9.9 mA . As a proof of concept, we will not pay too much attention to current consumption.

|  | Measurement |  | Simulation |  |
| :---: | :---: | :---: | :---: | :---: |
|  | HD | FD | HD | FD |
| Gain | 16.3 dB | 13.7 dB | 26.8 dB** | 13.7 dB** |
| Bandwidth ${ }^{*}$ | $6.7-7.9 \mathrm{GHz}$ | $6.6-8.0 \mathrm{GHz}$ | $6.7-7.9 \mathrm{GHz}$ | $6.6-8.0 \mathrm{GHz}$ |
| Rejection: <br> @ 2.4 GHz | 31.88 dB | 34.20 dB | 27.33 dB ** | 30.15 dB** |
| @ 5 GHz | 20.90 dB | 23.29 dB | 18.71 dB | 21.87 dB |
| @ 5.8 GHz | 20.81 dB | 23.51 dB | 6.1 dB | 9.67 dB |
| $S_{11}$ : <br> Minimum ${ }^{\dagger}$ <br> Desired ${ }^{\S}$ | $\begin{aligned} & 6.2-8.1 \mathrm{GHz} \\ & 7.2-7.6 \mathrm{GHz} \end{aligned}$ | $\begin{gathered} \text { 6.2-8.1 GHz } \\ \text { N/A } \end{gathered}$ | $\begin{gathered} 6.1-8.9 \mathrm{GHz} \\ 8.1 \mathrm{GHz} \end{gathered}$ | $\begin{aligned} & 6.2-9.2 \mathrm{GHz} \\ & 7.6-8.2 \mathrm{GHz} \end{aligned}$ |
| $S_{22}$ : <br> Minimum ${ }^{\dagger}$ <br> Desired ${ }^{\S}$ | $\begin{aligned} & 1-10 \mathrm{GHz} \\ & 7-7.2 \mathrm{GHz} \end{aligned}$ | $\begin{gathered} 1-10 \mathrm{GHz} \\ 7.5 \mathrm{GHz} \end{gathered}$ | $\begin{gathered} 7.4-10 \mathrm{GHz} \\ \mathrm{~N} / \mathrm{A} \end{gathered}$ | $\begin{gathered} \text { 7.4-10 GHz } \\ \text { N/A } \end{gathered}$ |
| $S_{33}$ : <br> Minimum ${ }^{\dagger}$ <br> Desired ${ }^{\S}$ | $\begin{aligned} & \text { N/A } \\ & \text { N/A } \end{aligned}$ | $\begin{aligned} & 1-10 \mathrm{GHz} \\ & \leq 2.6 \mathrm{GHz} \end{aligned}$ | $\begin{aligned} & \text { N/A } \\ & \text { N/A } \end{aligned}$ | $\begin{aligned} & 1-10 \mathrm{GHz} \\ & \leq 2.6 \mathrm{GHz} \end{aligned}$ |
| Reverse isolation | $>50 \mathrm{~dB}$ | $>50 \mathrm{~dB}$ | 70 dB | 50 dB |
| TX-RX isolation | N/A | 5 dB | N/A | $\geq 12.51 \mathrm{~dB}$ |
| Stability, $\mu$-factor | $\geq 3.95$ | $\geq 4.46$ | $\geq 3.35$ | $\geq 4.46$ |
| NF | N/A | N/A | $\leq 5.68 \mathrm{~dB}$ | $\leq 9.76 \mathrm{~dB}$ |
| P1dB | $-19 \mathrm{dBm}$ | $-22.1 \mathrm{dBm}$ | $-19 \mathrm{dBm}$ | $-22.1 \mathrm{dBm}$ |
| DC current | 13.8 mA | 23.7 mA | 27 mA | 37.4 mA |
| Area | $1.92 \mathrm{x} 0.86 \mathrm{~mm}^{2}$ |  |  |  |
| Supply voltage | 1.4 V |  | 1.5 V |  |
| $\begin{aligned} & { }^{*}-10 \mathrm{~dB} \\ & \dagger \leq-10 \mathrm{~dB} \\ & \S_{* *}^{*} \leq-20 \mathrm{~dB} \\ & { }^{\dagger} \text { Buffer de-embed } \end{aligned}$ |  |  |  |  |

### 5.2 Area-optimized RFFE

The area-optimized RFFE, which is a single-ended to differential structure, has a a single-ended $50 \Omega$ probe at the input, and a differential $100 \Omega$ probe pair at the output.
The maximum return loss ( $S_{11}$ ) of the RFFE, for a differential source impedance of $50 \Omega$ is measured at 7.875 GHz and is -13.13 dB . The $S_{11}$ -10 dB -bandwidth is $6.8-10 \mathrm{GHz}$.


Figure 5.21: $S_{11}$ of the area-optimized RFFE.


Figure 5.22: Real \& imaginary input impedance of the area-optimized RFFE.

The maximum output return loss $\left(S_{22}\right)$ of the RFFE, for a differential load impedance of $100 \Omega$ is measured at 8.375 GHz and is -10.56 dB . The $S_{22}-10 \mathrm{~dB}$-bandwidth is $1-10 \mathrm{GHz}$.


Figure 5.23: $S_{22}$ of the SEDERFFE.


Figure 5.24: Real \& imaginary output impedance of the area-optimized RFFE.

The voltage gain $\left(A_{V}\right)$ is reported as the maximum gain, and the bandwidth is reported as the -10 dB -bandwidth. In this RFFE, there was no de-embedding of output buffers. The maximum gain at the resonant frequency 8.0 GHz is 16.9 dB . The -10 dB BW is 1.3 GHz . Relative to the resonant frequency the BW is $\pm 650 \mathrm{MHz}$.

Attenuation at the bands of interest are:

$$
\text { Rejection }=\left\{\begin{array}{l}
\geq 45.21 \mathrm{~dB}, f \in[0,2.4] \mathrm{GHz},  \tag{5.4}\\
\geq 16.43 \mathrm{~dB}, f \in[2.4,5.0] \mathrm{GHz}, \\
18.81 \mathrm{~dB}, f=5.8 \mathrm{GHz}
\end{array}\right.
$$



Figure 5.25: Forward voltage gain of the area-optimized RFFE

The minimum reverse isolation is -58.33 at 7.875 GHz (passband).

For values lower than -50 dB , we start approaching the noise floor of the measurement setup. Thus, all signals lower than -50 dB , will be reported as $<-50 \mathrm{~dB}$.


Figure 5.26: Reverse isolation of the area-optimized RFFE
For stability, we use the $\mu$-stability test, if $\mu>1$, the RFFE is unconditionally stable. The minimum value of $\mu$ is $\mu_{\text {min }}=3.37$ at 8.375 GHz (passband).
As shown in Fig. 5.27, we meet the condition for unconditionally stability from $6-10 \mathrm{GHz}$.


Figure 5.27: $\mu$-factor for stability
For the 1 dB compression point ( P 1 dB ), we use large signal S-parameters (LSSP), where we sweep the input power, $P_{R F}$. From Fig. 5.28 , the P 1 dB is -22 dBm , which is 8 dB higher than required.


Figure 5.28: Large signal S-parameter, $S_{21}$, for area-optimized RFFE

The total current consumption, $I_{D C}$ of the area-optimized RFFE in is 13.8 mA . The active core consumes 7.3 mA and the buffers 6.5 mA . As a proof of concept, we will not pay too much attention to current consumption.

|  | Measurement | Simulation |
| :---: | :---: | :---: |
| Gain | 16.9 dB | $24.57 \mathrm{~dB}^{* *}$ |
| Bandwidth $^{*}$ | $7.4-8.7 \mathrm{GHz}$ | $7.4-8.8 \mathrm{GHz}$ |
| Rejection: |  |  |
| @ 2.4 GHz | 45.21 dB | $37.45 \mathrm{~dB}^{* *}$ |
| @ 5 GHz | 16.43 dB | $10.97 \mathrm{~dB}^{* *}$ |
| @ 5.8 GHz | 18.81 dB | $10.54 \mathrm{~dB}^{* *}$ |
| $S_{11}:$ |  |  |
| Minimum $^{\dagger}$ | $6.8-10 \mathrm{GHz}$ | $6.5-9.7 \mathrm{GHz}$ |
| Desired $^{\S}$ | $>9.2 \mathrm{GHz}$ | $7.1-7.6 \mathrm{GHz}$ |
| $S_{22}:$ | $1-10 \mathrm{GHz}$ | $1-10 \mathrm{GHz}$ |
| Minimum |  |  |
| Desired |  |  |

### 5.3 Summary

For the area optimized and passively configurable RFFEs, the measurement results closely match with specifications. For the AO-RFFE, rejection at 2.4 GHz and $5,5.8 \mathrm{GHz}$ is off by $\approx 2$ and $\approx 4 \mathrm{~dB}$, respectively compared to HD PR-RFFE. Voltage gain compared to HD PR-RFFE is practically the same. For the FD PR-RFFE, we expect a 3 dB loss in gain.

|  | PR-RFFE |  | AO-RFFE |
| :---: | :---: | :---: | :---: |
|  | HD | FD |  |
| Gain | 16.3 dB | 13.7 dB | 16.9 dB |
| Bandwidth* | $6.7-7.9 \mathrm{GHz}$ | $6.6-8.0 \mathrm{GHz}$ | $7.4-8.7 \mathrm{GHz}$ |
| Rejection: |  |  |  |
| @ 2.4 GHz | 31.88 dB | 34.20 dB | 45.21 dB |
| @ 5 GHz | 20.90 dB | 23.29 dB | 16.43 dB |
| @ 5.8 GHz | 20.81 dB | 23.51 dB | 18.81 dB |
| $S_{11}$ : |  |  |  |
| Minimum ${ }^{\dagger}$ | $6.2-8.1 \mathrm{GHz}$ | $6.2-8.1 \mathrm{GHz}$ | $6.8-10 \mathrm{GHz}$ |
| Desired ${ }^{\S}$ | $7.2-7.6 \mathrm{GHz}$ | N/A | $>9.2 \mathrm{GHz}$ |
| $S_{22}$ : |  |  |  |
| Minimum ${ }^{\dagger}$ | $1-10 \mathrm{GHz}$ | $1-10 \mathrm{GHz}$ | $1-10 \mathrm{GHz}$ |
| Desired ${ }^{\S}$ | $7-7.2 \mathrm{GHz}$ | 7.5 GHz | $\leq 2.4 \mathrm{GHz}$ |
| $S_{33}$ : |  |  |  |
| Minimum ${ }^{\dagger}$ | N/A | $1-10 \mathrm{GHz}$ | N/A |
| Desired ${ }^{\S}$ | N/A | $\leq 2.6 \mathrm{GHz}$ | N/A |
| Reverse isolation | $>50 \mathrm{~dB}$ | $>50 \mathrm{~dB}$ | $>50 \mathrm{~dB}$ |
| TX-RX isolation | N/A | 5 dB | N/A |
| Stability, $\mu$-factor | $\geq 3.95$ | $\geq 4.46$ | $\geq 3.37$ |
| NF | N/A | N/A | N/A |
| P1dB | $-19 \mathrm{dBm}$ | -22.1 dBm | $-22.1 \mathrm{dBm}$ |
| DC current | 13.8 mA | 23.7 mA | 13.8 mA |
| Area | $1.92 \times 0.86 \mathrm{~mm}^{2}$ |  | $1.1 \mathrm{x} 0.86 \mathrm{~mm}^{2}$ |
| Supply voltage | 1.4 V | 1.5 V |  |
| * -10 dB |  |  |  |
| $\dagger \leq-10 \mathrm{~dB}$ |  |  |  |
| $\S \leq-20 \mathrm{~dB}$ |  |  |  |

## Chapter 6

## Conclusion

In this work, we present two passive high-pass filters (HPF), a wideband reactive low-noise amplifier (LNA), a 'passively reconfigurable' RF frontend (RFFE) and an area efficient RFFE comprising said components. The reconfigurable RFFE can operate in half-duplex (HD) and fullduplex (FD) mode. In it's natural HD-mode, it is configured as pseudodifferential to pseudo-differential. In FD-mode, it is configured as singleended to pseudo-differential. All blocks are realized in 65 nm CMOS.
The filter in the reconfigurable RFFE is a $4^{\text {th }}$-order, pseudo-differential double harmonic trap (DHT) with $7 \mathrm{GHz}-3 \mathrm{~dB}$ cut-off frequency and an insertion loss of $<3 \mathrm{~dB}$. Notches at $4.8 \mathrm{GHz} \& 5.6 \mathrm{GHz}$ offer a minimum of 20 dB broadband $(1500 \mathrm{MHz})$ rejection. The filter in the area efficient RFFE is a $4^{\text {th }}$-order, single-ended to differential single harmonic trap (SHT) with a wideband on-chip $1: \sqrt{2}$ balun with a coupling coefficient of 0.82 . For single-ended termination, the source impedance is $50 \Omega$. The cut-off frequency and insertion loss are 7.4 GHz and $<3.1 \mathrm{~dB}$, respectively. Notch at 5.7 GHz offers a minimum of 15 dB rejection. The die area of the DHT and SHT HPFs is $0.757 \mathrm{~mm}^{2}$ and $0.473 \mathrm{~mm}^{2}$ respectively. The HPFs, especially the DHT can be used to overcome interference problems and improve coexistence of IEEE 802.15.z (e.g., 6 -10 GHz) and IEEE 802.11a wireless RF systems.
Standalone, the $7.3-8.8 \mathrm{GHz}$ tuned low-noise amplifier (LNA) provides at least 0 dB out-of-band rejection at frequencies in the 5 GHz -band. The power-to-voltage ( $\mathrm{P}-\mathrm{V}$ ) configured LNA comprises of reactivefeedback and feedforward loops; negative current-current and positive voltage-voltage loops for impedance matching and $g_{m}$-boosting. The LNA achieves $22.3 \pm 5 \mathrm{~dB}$ gain, $4.29 \pm 0.07 \mathrm{~dB}$ noise figure at 6 mA and 1.5 V supply with $-17 \mathrm{dBm} 1-\mathrm{dB}(\mathrm{P} 1 \mathrm{~dB})$, demonstrating design parameters well suited for impulse-radio ultra-wideband (IR-UWB). The die area of the LNA is $0.72 \mathrm{~mm}^{2}$.
For the DHT and LNA configuration (in HD-mode), notches at 4.8 GHz and 5.6 GHz offer a minimum of -15 dB broadband $(1400 \mathrm{MHz})$ rejection. The combined gain and noise figure $18 \pm 5 \mathrm{~dB}$ and $5.5 \pm 0.1 \mathrm{~dB}$, respectively. For FD-mode, there is a design flaw that need to be addressed, i.e., the impedance mismatch between the core and auxiliarybranches of the

LNA. This directly affects the amplitude and phase of the 'copied' signal required for cancelling out the transmitted signal.
Moreover, because of the limitation in the measurement setup, and higher than expected losses at the interface of the DHT HPF \& LNA, we cannot accurately de-embed the TX-ANT/ANT-RX isolation.
For the area efficient RFFE (i.e., the SHT HPF and LNA configuration) provides a minimum of -10 dB rejection with a single notch at 5.7 GHz . The combined gain and noise figure is $19.57 \pm 5 \mathrm{~dB}$ and $7.25 \pm 0.25 \mathrm{~dB}$, respectively. This design demonstrates on how to embed the self-inductance of the balun windings as part of the ladder filter implementation.
Measured results closely match specifications.

## Future work

Configurable impedance matching network for TX-auxiliary For maximum TX-RX isolation we require amplitude and phase matching of the cancelling signal. Instead of a 'fixed' impedance for the auxiliary branch, we can use a configurable/switchable RC network. This provides tunability, and robustness to process-voltage-temperature (PVT) variations.

PVT For the LC tank in the LNA, we can replace the fixed capacitor with a trimmable switched-capacitor to compensate PVT effects. Similarly, adding switched-capacitor banks for the notch(es) in the HPFs provides frequency tuning.

Wi-Fi 6 GHz-band IN 2020, the Federal Communications Commission (FCC) opened up the 6 GHz -band for unlicensed use [20]. The 6 GHz band stretches from $5.925-7.125 \mathrm{GHz}$. With the RFFEs in this work, these bands are a significant problem with regards to reliability. For future UWB-RFFEs, one requires significant rejection at 2.4 GHz -band, 5 GHz band as well as the new 6 GHz -band if UWB-RFFEs are to co-exist with licensed devices.

## Bibliography

[1] J. Yoshida. 'iPhone 11, Watch 5: What's New, What's Dropped'. In: EETimes (10th Apr. 2019). URL: https://www.eetimes.com/iphone-11-watch-5-whats-new-whats-dropped/(visited on 12/12/2020).
[2] S. Asif. 'Galaxy Note 20 Ultra's UWB tech to be used for faster file sharing, digital keys'. In: SamMobile (6th Aug. 2020). URL: https: / /www.sammobile.com / news / galaxy- note-20-ultra- uwb-faster-file-transfers-digital-keys/ (visited on $12 / 12 / 2020$ ).
[3] Decawave Ltd. DW1000 Datasheet. Version 2.22. 2017. url: https: / / www . decawave . com / product / dw1000-radio - ic/ (visited on 12/12/2020).
[4] N. Dahad. 'Lenovo adds Novelda UWB sensor for human presence detection in ThinkPad'. In: Embedded (5th Nov. 2020). URL: https: / /www.embedded.com/lenovo-adds-novelda-uwb-sensor- for-human-presence-detection-in-thinkpad/.
[5] N. Andersen et al. 'A 118-mW Pulse-Based Radar SoC in 55-nm CMOS for Non-Contact Human Vital Signs Detection'. In: IEEE Journal of Solid-State Circuits 52.12 (2017), pp. 3421-3433. DOI: 10.1109/JSSC.2017.2764051.
[6] X. Yi et al. 'A 3.4-4.6GHz In-Band Full-Duplex Front-End in CMOS Using a Bi-Directional Frequency Converter'. In: 2020 IEEE Radio Frequency Integrated Circuits Symposium (RFIC). 2020, pp. 47-50. DOI: 10.1109/RFIC49505.2020.9218445.
[7] Heimo Uhrmann, Robert Kolm and Horst Zimmermann. Analog Filters in Nanometer CMOS. eng. Vol. 45. Springer Series in Advanced Microelectronics. Springer Berlin / Heidelberg, 2013. ISBN: 9783642430299.
[8] Analog Devices Inc. Linear circuit design handbook. eng. 1st edition. Elsevier/Newnes Press, 2008. ISBN: 1-281-27326-0.
[9] David M. Pozar. Microwave engineering. eng. 4th ed. Chichester: Wiley, 2012. ISBN: 9780470631553.
[10] Hooman Darabi. Radio Frequency Integrated Circuits and Systems. eng. 1st ed. Cambridge University Press, 2015. ISBN: 9780521190794.
[11] J. R. Long. 'Monolithic transformers for silicon RF IC design'. In: IEEE Journal of Solid-State Circuits 35.9 (Sept. 2000), pp. 13681382. ISSN: 1558-173X. DOI: 10.1109/4.868049.
[12] B. Razavi. Design of Analog CMOS Integrated Circuits. 2nd ed. McGraw-Hill Higher Education, 2016. ISBN: 9780077496128.
[13] A.M. Niknejad. Electromagnetics for High-Speed Analog and Digital Communication Circuits. 1st ed. Cambridge University Press, 2007. ISBN: 9781139462242.
[14] T.H. Lee. The Design of CMOS Radio-Frequency Integrated Circuits. 2nd ed. Cambridge University Press, 2004. ISBN: 9780521835398.
[15] B. Razavi. RF Microelectronics. 2nd ed. Pearson Education International, 2012. ISBN: 9780132839419.
[16] Xiaoyong Li, S. Shekhar and D. J. Allstot. ' $G_{m}$-boosted commongate LNA and differential colpitts VCO/QVCO in $0.18-\mu \mathrm{m}$ CMOS'. In: IEEE Journal of Solid-State Circuits 40.12 (2005), pp. 26092619. DOI: 10.1109/JSSC.2005.857426.
[17] B.E. Boser. 'Analog Design Using $g_{m} / I_{d}$ and $f_{t}$ Metrics'. In: (2011). URL: https: / / people.eecs.berkeley. edu / ~boser / presentations / 201112\%200TA \% 20gm \% 20Id.pdf.
[18] National Instruments. Advantages of Direct RF Sampling Architectures. National Instruments. 5th Mar. 2019. URL: https: / / www.ni.com/en-no/innovations/white-papers/18/advantages-of-direct-rf-sampling-architectures.html (visited on 14/02/2020).
[19] Tommy Neu. Direct RF conversion: From vision to reality. Texas Instruments. May 2015. URL: http://www.ti.com/lit/slyy068 (visited on $14 / 12 / 2020$ ).
[20] Federal Communications Commision. FCC Opens 6 GHz Band to Wi-Fi and Other Unlicensed Uses. Federal Communications Commision. 23rd Apr. 2020. URL: https://www.fcc.gov/document/ fcc-opens-6-ghz-band-wi-fi-and-other-unlicensed-uses (visited on $13 / 12 / 2020)$.


[^0]:    * @ 5.8 GHz
    ${ }^{\dagger}$ Driven differentially

[^1]:    ${ }^{1}$ The CS-stage has a phase shift of $180^{\circ}$ compared to the CG-stage.

[^2]:    ${ }^{2}$ Ground bounce as in the ground is not longer at a zero voltage potential, but has a certain voltage due to parasitic resistance in the power routing.

[^3]:    ${ }^{3}$ With the buffer deembeded

[^4]:    * Buffer deembedded
    ${ }^{\dagger}-10 \mathrm{~dB}$
    ${ }^{\ddagger} S_{11} \leq-10 \mathrm{~dB}$
    $\S_{S_{11}} \leq-20 \mathrm{~dB}$
    ${ }^{\|} S_{22} \leq-10 \mathrm{~dB}$
    ${ }^{\text {a }} S_{22} \leq-20 \mathrm{~dB}$

